

# Indistinguishability Obfuscation from Well-Founded Assumptions

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### ABSTRACT

Indistinguishability obfuscation, introduced by [Barak et. al. Crypto 2001], aims to compile programs into unintelligible ones while preserving functionality. It is a fascinating and powerful object that has been shown to enable a host of new cryptographic goals and beyond. However, constructions of indistinguishability obfuscation have remained elusive, with all other proposals relying on heuristics or newly conjectured hardness assumptions.

In this work, we show how to construct indistinguishability obfuscation from subexponential hardness of four well-founded assumptions. We prove:

**Informal Theorem:** Let  $\tau \in (0, \infty)$ ,  $\delta \in (0, 1)$ ,  $\epsilon \in (0, 1)$  be arbitrary constants. Assume sub-exponential security of the following assumptions:

- the Learning With Errors (LWE) assumption with subexponential modulus-to-noise ratio  $2^{k^{\epsilon}}$  and noises of magnitude polynomial in k, where k is the dimension of the LWE secret,

- the Learning Parity with Noise (LPN) assumption over general prime fields  $\mathbb{Z}_p$  with polynomially many LPN samples and error rate  $1/\ell^{\delta}$ , where  $\ell$  is the dimension of the LPN secret,

- the existence of a Boolean Pseudo-Random Generator (PRG) in NC<sup>0</sup> with stretch  $n^{1+\tau}$ , where n is the length of the PRG seed,

- the Decision Linear (DLIN) assumption on symmetric bilinear groups of prime order.

Then, (subexponentially secure) indistinguishability obfuscation for all polynomial-size circuits exists. Further, assuming only polynomial security of the aforementioned assumptions, there exists collusion resistant public-key functional encryption for all polynomial-size circuits.

# **CCS CONCEPTS**

• Theory of computation  $\rightarrow$  Cryptographic primitives.

# **KEYWORDS**

indistinguishability obfuscation

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### **1** INTRODUCTION

In this work, we study the notion of indistinguishability obfuscation (*iO*) for general polynomial-size circuits [19, 66, 77]. *iO* requires that for any two circuits  $C_0$  and  $C_1$  of the same size, such that  $C_0(x) = C_1(x)$  for all inputs x, we have that  $iO(C_0)$ is computationally indistinguishable to  $iO(C_1)$ . Furthermore, the obfuscator *iO* should be computable in probabilistic polynomial time. The notion of *iO* has proven to be very powerful, with over a hundred papers published utilizing *iO* to enable a remarkable variety of applications in cryptography and complexity theory; indeed *iO* has even expanded the scope of cryptography (see, e.g. [17, 26, 44, 57, 58, 66, 67, 76, 83, 83, 84, 99, 111]).

Despite this success, until this work, all previously known iO constructions (see [68] and the references therein) required new hardness assumptions that were postulated specifically for showing security of the iO schemes proposed. Indeed, the process of understanding these assumptions has been tortuous, with several of these assumptions broken by clever cryptanalysis [18, 21, 33, 40, 53, 54, 60, 82, 85, 103, 105, 106]. The remaining standing ones are based on new and novel computational problems that are different in nature from well-studied computational problems (for instance, LWE with leakage on noises).

As a result, there has been a lack of clarity about the state of iO security [24]. Our work aims to place iO on *terra firma*.

*Our contribution.* We show how to construct *iO* from subexponential hardness of four well-founded assumptions. We prove:

THEOREM 1.1. (Informal) Let  $\tau$  be arbitrary constants greater than 0, and  $\delta$ ,  $\epsilon$  in (0, 1). Assume sub-exponential security of the following assumptions, where  $\lambda$  is the security parameter, p is a  $\lambda$ -bit prime, and the parameters  $\ell$ , k, n below are large enough polynomials in  $\lambda$ :

- the LWE assumption over Z<sub>p</sub> with subexponential modulusto-noise ratio 2<sup>k<sup>€</sup></sup> and noises of magnitude polynomial in k, where k is the dimension of the LWE secret,
- the LPN assumption over general prime fields Z<sub>p</sub> with polynomially many LPN samples and error rate 1/ℓ<sup>δ</sup>, where ℓ is the dimension of the LPN secret,
- the existence of a Boolean PRG in  $NC^0$  with stretch  $n^{1+\tau}$ ,
- the DLIN assumption on symmetric bilinear groups of prime order.

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Then, (subexponentially secure) indistinguishability obfuscation for all polynomial-size circuits exists.

All four assumptions are based on computational problems with a long history of study, rooted in complexity, coding, and number theory. Further, they were introduced for building basic cryptographic primitives (such as public key encryption), and have been used for realizing a variety of cryptographic goals that have nothing to do with iO.

# 1.1 Assumptions in More Detail

We now describe each of these assumptions in more detail and briefly survey their history. Throughout the paper, when we say that a primitive or assumption is polynomially or subexponentially hard (alternatively, secure or indistinguishable), we mean that the hardness of the primitive or assumption holds against all adversaries that run in *polynomial* time in the security parameter  $\lambda$  with at most negligible or subexponential advantage  $2^{-\lambda^{\epsilon}}$  for some  $\epsilon > 0$ respectively. More precisely, the security level of the assumptions below is first measured in their own parameters, namely, the modulus length  $\lceil \log p \rceil$  of pairing groups for DLIN over symmetric bilinear groups, the dimension k of the LWE secret, the dimension  $\ell$  of the LPN secret, and the seed length *n* of the PRG in NC<sub>0</sub>. In our construction, modulus length  $\lceil \log p \rceil$  is set to  $\lambda$ , and  $k, \ell, n$  are sufficiently large polynomials in  $\lambda$ . Therefore, polynomial or subexponential hardness of these assumptions measured in their own parameters translates into polynomial or subexponential hardness in λ.

*The* DLIN *Assumption:* The Decisional Linear assumption (DLIN) is stated as follows: For an appropriate  $\lambda$ -bit prime p, two groups  $\mathbb{G}$  and  $\mathbb{G}_T$  are chosen of order p such that there exists an efficiently computable nontrivial symmetric bilinear map  $e : \mathbb{G} \times \mathbb{G} \to \mathbb{G}_T$ . A canonical generator g for  $\mathbb{G}$  is also computed. Then, the DLIN assumption requires that the following computational indistinguishability holds:

$$\{ (g^x, g^y, g^{xr}, g^{ys}, g^{r+s}) \mid x, y, r, s \leftarrow \mathbb{Z}_p \}$$
  
 
$$\approx_c \{ (g^x, g^y, g^{xr}, g^{ys}, g^z) \mid x, y, r, s, z \leftarrow \mathbb{Z}_p \}$$

This assumption was first introduced in the 2004 work of Boneh, Boyen, and Shacham [30]. Since then DLIN and assumptions implied by DLIN have seen extensive use in a wide variety of applications throughout cryptography, such as Identity-Based Encryption, Attribute-Based Encryption, Functional Encryption for degree 2 polynomials, Non-Interactive Zero Knowledge, etc. (See, e.g. [23, 42, 81, 93, 109]).

*The* LWE *Assumption:* The Learning With Errors LWE assumption with respect to a modulus q, dimension k, sample complexity n, and discrete Gaussian distribution  $\chi$  over integers states that the following computational indistinguishability holds:

$$\{\mathbf{A}, \mathbf{s} \cdot \mathbf{A} + \mathbf{e} \mod q \mid \mathbf{A} \leftarrow \mathbb{Z}_q^{k \times n}, \ \mathbf{s} \leftarrow \mathbb{Z}_q^{1 \times k}, \ \mathbf{e} \leftarrow \chi^{1 \times n}\}$$
$$\approx_c \{\mathbf{A}, \mathbf{u} \mid \mathbf{A} \leftarrow \mathbb{Z}_q^{k \times n}, \ \mathbf{u} \leftarrow \mathbb{Z}_q^{1 \times n}\}$$

In this work, the dimension of the secret k is a sufficiently large polynomial in  $\lambda$ . Moreover, the sample complexity is polynomial

n(k), and we require the modulus-to-noise ratio to be subexponential in k, i.e.,  $2^{k^{\epsilon}}$  for an arbitrary  $\epsilon > 0$ , and the magnitude of the noises to be polynomial in k.

This assumption was first stated in the work of [110]. LWE has been used extensively to construct applications such as Leveled Fully Homomorphic Encryption [41, 43, 71], Lockable Obfuscation [80, 114], Attribute Based Encryption [32, 78, 79] and Universal Thresholdizers [31].

The existence of PRGs in NC<sup>0</sup>: The assumption of the existence of a Boolean Pseudo-Random Generator PRG in NC<sup>0</sup> states that there exists a Boolean function  $G : \{0, 1\}^n \rightarrow \{0, 1\}^m$  where  $m = n^{1+\tau}$ for some constant  $\tau > 0$ , and where each output bit computed by G depends on a constant number of input bits, such that the following computational indistinguishability holds:

$$\{G(\boldsymbol{\sigma}) \mid \boldsymbol{\sigma} \leftarrow \{0,1\}^n\} \approx_c \{\boldsymbol{y} \mid \boldsymbol{y} \leftarrow \{0,1\}^m\}$$

Pseudorandom generators are a fundamental primitive in their own right, and have vast applications throughout cryptography. PRGs in NC<sup>0</sup> are tightly connected to the fundamental topic of Constraint Satisfaction Problems (CSPs) in complexity theory, and were first proposed for cryptographic use by Goldreich [62, 75, 87] 20 years ago. The complexity theory and cryptography communities have jointly developed a rich body of literature on the cryptanalysis and theory of constant-locality Boolean PRGs [9, 10, 12, 13, 15, 16, 28, 29, 59, 61, 62, 75, 101, 107, 108].

LPN over large fields: Like LWE, the Learning Parity with Noise LPN assumption over finite fields  $\mathbb{Z}_p$  is also a decoding problem. The standard LPN assumption with respect to subexponential-size modulus p, dimension  $\ell$ , sample complexity n, and a noise rate  $r = 1/\ell^{\delta}$  for some  $\delta \in (0, 1)$ , states that the following computational indistinguishability holds:

$$\{\mathbf{A}, \mathbf{s} \cdot \mathbf{A} + \mathbf{e} \mod p \mid \mathbf{A} \leftarrow \mathbb{Z}_p^{\ell \times n}, \ \mathbf{s} \leftarrow \mathbb{Z}_p^{1 \times \ell}, \ \mathbf{e} \leftarrow \mathcal{D}_r^{1 \times n}\}$$
$$\approx_c \{\mathbf{A}, \mathbf{u} \mid \mathbf{A} \leftarrow \mathbb{Z}_p^{\ell \times n}, \ \mathbf{u} \leftarrow \mathbb{Z}_p^{1 \times n}\}.$$

Above  $e \leftarrow \mathcal{D}_r$  is a generalized Bernoulli distribution, *i.e.* e is sampled randomly from  $\mathbb{Z}_p$  with probability  $1/\ell^{\delta}$  and set to be 0 with probability  $1 - 1/\ell^{\delta}$ . Thus, the difference between LWE and LPN is the structure of the error distribution. In LWE the error vector is a random (polynomially) bounded vector. In LPN, it is a random sparse vector, but where it is nonzero, the entries have large expectation. Again, we consider polynomial sample complexity  $n(\ell)$ , and the modulus p is an arbitrary subexponential function in  $\ell$ .

The origins of the LPN assumption date all the way back to the 1950s: the works of Gilbert [74] and Varshamov [112] showed that random linear codes possessed remarkably strong minimum distance properties. However, since then, almost no progress has been made in efficiently decoding random linear codes under random errors. The LPN over fields assumption above formalizes this, and was introduced over  $\mathbb{Z}_2$  for cryptographic uses in 1994 [27], and formally defined for general finite fields and parameters in 2009 [88], under the name "Assumption 2".

While in [88], the assumption was used when the error rate is constant, in fact, polynomially low error (in fact  $\delta = 1/2$ ) has an even longer history in the LPN literature: it was used by Alekhnovitch

in 2003 [4] to construct public-key encryption with the field  $\mathbb{F}_2$ , and used to build public-key encryption over  $\mathbb{F}_p$  in 2015 [11]. The exact parameter settings that we describe above, with both general fields and inverse polynomial error rate corresponding to an arbitrarily small constant  $\delta > 0$  was explicitly posed by [35], in the context of building efficient secure two-party and multi-party protocols for arithmetic computations.

Recently, the LPN assumption has led to a wide variety of applications (see for example, [11, 14, 22, 35, 36, 63, 73, 88]). A comprehensive review of known attacks on LPN over large fields, for the parameter settings we are interested in, was given in [35, 37]. For our parameter setting, the running time of all known attacks is  $\Omega(2^{\ell^{1-\delta}})$ , for any choice of the constant  $\delta \in (0, 1)$  and for any polynomial number of samples  $n(\ell)$ .

The works of [10, 15] showed that the one-wayness of random local functions implies the existence of PRGs in NC<sup>0</sup>. More precisely, for a length parameter m = m(n), a locality parameter  $\hat{d} = O(1)$ , and a *d*-ary predicate  $Q : \{0, 1\}^d \to \{0, 1\}$ , a distribution  $\mathcal{F}_{Q,m}$ samples a *d*-local function  $f_{G,Q} : \{0,1\}^d \to \{0,1\}$  by choosing a random d-uniform hypergraph G with n nodes and m hyperedges, where each hyperedge is chosen uniformly and independently at random. The *i*'th output bit of  $f_{G,Q}$  is computed by evaluating Qon the *d* input bits indexed by nodes in the *i*'th hyperedge. The one-wayness of  $\mathcal{F}_{Q,m}$  for proper choices of Q, m has been conjectured and studied in [12, 28, 59, 75, 107]. The works of [10, 15] showed how to construct a family of PRG in NC<sup>0</sup> with polynomial stretch based on the one-wayness of  $\mathcal{F}_{Q,m}$  for any Q that is sensitive (i.e., some input bit *i* of *Q* has full influence) and any  $m = n^{1+\delta}$ with  $\delta > 0$ . The constructed PRGs have negligible distinguishing advantage and the reduction incurs a multiplicative polynomial security loss. Therefore, the subexponential pseudorandomness of PRG in NC<sup>0</sup> that we need is implied by the existence of  $\mathcal{F}_{Q,m}$  that is hard to invert with noticable probability by adversaries of some subexponential size.

#### 1.2 Our Ideas in a Nutshell

Previous works [6, 7, 89, 90, 102] led to the recent construction of iO in [68] based on LWE, DLIN over symmetric bilinear groups, and one other object, that we will encapsulate as a *structured-seed* PRG (sPRG) with polynomial stretch and special efficiency properties<sup>1</sup>. In an sPRG, the seed consists of both a public and private part. The pseudorandomness property of the sPRG should hold even when the adversary can see the public seed in addition to the output of the sPRG. Crucially, the output of the sPRG should be computable by a *degree-2* computation in the private seed (where the coefficients of this degree-2 computation are obtained through constant-degree computations on the public seed).

Our key innovation is a simple way to leverage LPN over fields to build an sPRG. The starting point for our construction is the following observation. Assuming LPN and that *G* is an (ordinary) PRG in NC<sup>0</sup> with stretch m(n), we immediately have the following STOC '21, June 21-25, 2021, Virtual, Italy

computational indistinguishability:

$$\{(A, b = s \cdot A + e + \sigma, G(\sigma))\} \approx_c \{(A, u, w)\}$$

where  $A \leftarrow \mathbb{Z}_p^{\ell \times n}$ ,  $s \leftarrow \mathbb{Z}_p^{1 \times \ell}$ ,  $e \leftarrow \mathcal{D}_r^{1 \times n}(p)$ ,  $\sigma \leftarrow \{0, 1\}^{1 \times n}$ ,  $u \leftarrow \mathbb{Z}_p^{1 \times n}$ , and  $w \leftarrow \{0, 1\}^{1 \times m(n)}$ . Roughly speaking, we can think of both A and b above as being

Roughly speaking, we can think of both A and b above as being public. All that remains is to show that the computation of  $G(\sigma)$ can be performed using a degree-2 computation in a short-enough specially-prepared secret seed. Because G is an arbitrary PRG in NC<sup>0</sup>, it will not in general be computable by a degree-2 polynomial in  $\sigma$ . To accomplish this goal, we crucially leverage the *sparseness* of the LPN error e. The evaluation of our sPRG can be viewed to take two steps: First, homomorphically evaluate the PRG on an LPN-encryption of the seed  $\sigma$  (i.e., A, b above) to obtain an LPNencryption of the PRG output  $G(\sigma)$  that is however corrupted with *sparse* errors. Next, decrypt and correct the sparse errors all in just degree 2, by means of a simple pre-computation idea. In particular, the precomputation compresses the *sparse* errors to be corrected into vectors of sublinear length that later can be expanded back using a degree 2 computation. A gentle overview is provided in Section 4, followed by our detailed construction and analysis.

#### **1.3 Implications of** *iO*

The notion of *iO* occupies an intriguing and influential position in complexity theory and cryptography. Interestingly, if NP  $\subseteq$  BPP, then *iO* exists for the class of all polynomial-size circuits, because if NP  $\subseteq$  BPP, then it is possible to efficiently compute a canonical form for any function computable by a polynomial-size circuit. On the other hand, if NP  $\not\subseteq$  io-BPP, then in fact the existence of *iO* for polynomial-size circuits implies that one-way functions exist [96]. And a large body of work has shown that *iO* plus one-way functions imply a vast array of cryptographic objects, so much so that *iO* has been conjectured to be a "central hub" [96, 111] for cryptography.

An impressive list of fascinating new cryptographic objects are only known under iO or related objects such as functional encryption and witness encryption. Hence, our construction of iOfrom well-founded assumptions immediately implies these objects from the same assumptions. Below, we highlight a subset of these implications as corollaries.

COROLLARY 1.2 (INFORMAL). Assume the subexponential hardness of the four assumptions in Theorem 1.1, we have:

- Multiparty non-interactive key exchange in the plain model (without trusted setup), e.g., [34, 95].
- Adaptively secure succinct garbled RAM, where the size of the garbled program is  $poly(\lambda, log T)|P|$  depending linearly on the description size of the RAM program P, the size of the garbled input is  $poly(\lambda)|x|$  depending linearly on the size of the input x, and evaluation time is quasilinear in the running time of P on x [5, 8, 25, 46, 48, 49, 52, 100].
- Indistinguishability obfuscation for RAM, where the size of obfuscated program is poly(λ, n, |P|) where |P| is the description size of the RAM program P and n is its input length [5, 8, 25, 46, 48, 49, 52, 100].
- Selectively sound and perfectly zero-knowledge Succinct Noninteractive ARGument (SNARG) for any NP language with statements up to a bounded polynomial size in the CRS model,

<sup>&</sup>lt;sup>1</sup>In early version of [68], the notion of sPRG was implicit in the sense that [68] directly proposed a candidate sPRG based on a new assumption without formalizing the notion of sPRG. They then constructed *iO* using this candidate sPRG, LWE, and DLIN. After this work formalized the notion of sPRG, [68] has updated their construction to replace their specific candidate with a general sPRG.

where the CRS size is  $poly(\lambda)(n + m)$ , n, m are upper bounds on the lengths of the statements and witnesses, and the proof size is  $poly(\lambda)$  [111]<sup>2</sup>.

- Sender deniable encryption [111], and fully deniable interactive encryption [51].
- Constant round concurrent zero-knowledge protocols for any NP language [57].
- (Symmetric or asymmetric) multilinear maps with boudned polynomial multilinear degrees, following [2, 3, 65], and selfbilinear map over composite and unknown order group, assuming additionally the polynomial hardness of factoring [115].
- Correlation intractable functions for all sparse relations verifiable in bounded polynoimal size, assuming additionally the polynomial hardness of input hiding obfuscators for evasive circuits [47], or for all sparse relations, assuming additionally the exponential optimal hardness of input hiding obfuscators for multibit point functions [94].
- Witness Encryption (WE) for any NP language, following as a special case of iO for polynomial size circuits.
- Secret sharing for any monotone function in NP [97].

COROLLARY 1.3 (INFORMAL). Assume the polynomial hardness of the four assumptions in Theorem 1.1, we have:

- Attribute Based Encryption (ABE) for unbounded-depth polynomial-size circuits, following as a special case of functional encryption for unbounded-depth polynomial size circuits.
- Fully homomorphic encryption scheme for unbounded-depth polynomial size circuits (without relying on circular security), assuming slighly superpolynomial hardness of the four assumptions [50].
- PPAD hardness [1, 26, 67, 86, 98].

Regarding PPAD hardness, another line of beautiful works [45, 55, 56, 64, 92, 104] showed that the hardness of #SAT reduces to that of PPAD, assuming the adaptive soundness of applying Fiat-Shamir to certain protocols. Most recently, this led to basing the PPAD hardness on that of #SAT and the sub-exponential LWE assumption [92]. In comparison, relying on [67], using our Functional Encryption construction, we can base the PPAD hardness on the polynomial security of the four assumptions we make.

#### 1.4 Other Recent Works

We briefly discuss three recent works  $[38, 69, 113]^3$  that develop a completely independent line of attack for constructing *iO*. At present this line still requires new hardness assumptions, but has other potential advantages, as we explain below.

Gay and Pass [69] proved the security of a variant of the candidate *iO* of [39], based on the subexponential hardness of *i*) LWE with subexponential modulus-to-noise ratio, and *ii*) a new circular security leakage resilience conjecture, referred to as *1-circular Shielded Randomness Leakage (SRL) resilient security*, on the Gentry, Sahai, and Waters (GSW) homomorphic encryption scheme [71]. The work of [38] proved the security of a similar construction based on a new variant of the 2-circular SRL security assumption proposed by [69], with respect to GSW and a packed variant of the dual-Regev encryption scheme [70], which requires SRL security to hold in the presence of a length-2 chain of circular encryptions of the secret keys of GSW and packed dual-Regev. They also suggest another assumption that may be a potential relaxation that considers a key-randomness encryption cycle, instead of a key cycle.

Wichs and Wee [113] proposed a different construction of *iO* based on a new encryption scheme called dual-GSW that "marries" together dual-Regev and GSW encryption schemes. Their new assumption also has a circular security flavor, but is significantly different from the circular SRL security that [38, 69] proposed.

Comparing with the above constructions, our construction has the advantage in relying solely on well-founded assumptions that have a long history of study. On the other hand, these constructions are based only on lattices and have the advantage of being plausibly quantum-secure, whereas the SXDH assumption on bilinear maps that our construction relies on is known to be broken by polynomialtime quantum algorithms.

# 2 PRELIMINARIES

For any distribution X, we denote by  $x \leftarrow X$  the process of sampling a value x from the distribution X. Similarly, for a set X we denote by  $x \leftarrow X$  the process of sampling x from the uniform distribution over X. For an integer  $n \in \mathbb{N}$  we denote by [n] the set  $\{1, ..., n\}$ . A function negl  $: \mathbb{N} \to \mathbb{R}$  is negligible if for every constant c > 0 there exists an integer  $N_c$  such that negl $(\lambda) < \lambda^{-c}$  for all  $\lambda > N_c$ . Throughout, when we refer to polynomials in security parameter, we mean constant degree polynomials that take positive value on non negative inputs. We denote by  $poly(\lambda)$  an arbitrary polynomial in  $\lambda$  satisfying the above requirements of nonnegativity. We denote vectors by bold-faced letters such as b and u. Matrices will be denoted by capitalized bold-faced letters for such as A and M. For any  $k \in \mathbb{N}$ , we denote by  $v^{\otimes k}$  the vector obtained by tensoring v with itself for k times. This vector contains all the monomials in the variables inside v of degree exactly k.

We also introduce two new notations. First, for any vector v we refer by dim(v) the dimension of vector v. For any matrix  $\mathbf{M} \in \mathbb{Z}_q^{n_1 \times n_2}$ , we denote by  $|\mathbf{M}|$  the bit length of  $\mathbf{M}$ . In this case,  $|\mathbf{M}| = n_1 \cdot n_2 \cdot \log_2 q$ . We also overload this operator in that, for any set S, we use |S| to denote the cardinality of S. The meaning should be inferred from context.

For any two polynomials  $a(\lambda, n), b(\lambda, n) : \mathbb{N} \times \mathbb{N} \to \mathbb{R}^{\geq 0}$ , we say that *a* is polynomially smaller than *b*, denoted as  $a \ll b$ , if there exists an  $\epsilon \in (0, 1)$  and a constant c > 0 such that  $a < b^{1-\epsilon} \cdot \lambda^c$  for all large enough  $\lambda, n \in \mathbb{N}$ . The intuition behind this definition is to think of *n* as being a sufficiently large polynomial in  $\lambda$ 

Multilinear Representation of Polynomials and Representation over  $\mathbb{Z}_p$ . A straightforward fact from analysis of boolean functions is that every NC<sup>0</sup> function  $F : \{0, 1\}^n \to \{0, 1\}$  can be represented by a unique constant degree multilinear polynomial  $f \in \mathbb{Z}[\mathbf{x} = (x_1, \ldots, x_n)]$ , mapping  $\{0, 1\}^n$  to  $\{0, 1\}$ . At times, we consider a mapping of such polynomial  $f \in \mathbb{Z}[\mathbf{x}]$  into a polynomial g over  $\mathbb{Z}_p[\mathbf{x}]$  for some prime p. This is simply obtained by reducing the coefficients of f modulo p and then evaluating the polynomial

<sup>&</sup>lt;sup>2</sup>This construction does not contradict the lower bound result by [72] showing that it is impossible to base the adaptive soundness of SNARGs on falsifiable assumptions via black-box reductions, since this construction only achieves selective soundness.

<sup>&</sup>lt;sup>3</sup>These works were posted online very shortly after the initial posting of our work.

over  $\mathbb{Z}_p$ . Observe that  $g(\mathbf{x}) = f(\mathbf{x}) \mod p$  for every  $\mathbf{x} \in \{0, 1\}^n$  as  $f(\mathbf{x}) \in \{0, 1\}$  for every such  $\mathbf{x}$ . Furthermore, given any NC<sup>0</sup> function *F*, finding these representations take polynomial time.

Definition 2.1 ( $\epsilon$ -indistinguishability). We say that two ensembles  $\mathcal{X} = \{\mathcal{X}_{\lambda}\}_{\lambda \in \mathbb{N}}$  and  $\mathcal{Y} = \{\mathcal{Y}_{\lambda}\}_{\lambda \in \mathbb{N}}$  are  $\epsilon$ -indistinguishable where  $\epsilon : \mathbb{N} \to [0, 1]$  if for every probabilistic polynomial time adversary  $\mathcal{A}$  it holds that: For every sufficiently large  $\lambda \in \mathbb{N}$ ,

$$\Pr_{x \leftarrow \mathcal{X}_{\lambda}} [\mathcal{A}(1^{\lambda}, x) = 1] - \Pr_{y \leftarrow \mathcal{Y}_{\lambda}} [\mathcal{A}(1^{\lambda}, y) = 1] \le \epsilon(\lambda).$$

We say that two ensembles are polynomially indistinguishable if they are  $\epsilon$ -indistinguishable for  $\epsilon(\lambda) = \operatorname{negl}(\lambda)$  for some negligible negl, and that two ensembles are sub-exponentially indistinguishable if they are  $\epsilon$ -indistinguishable for  $\epsilon(\lambda) = 2^{-\lambda^c}$  for some positive real number *c*.

Below if the security a primitive or the hardness of an assumption are defined through indistinguishability, we say the primitive or assumption is  $\epsilon$  secure, hard, or indistinguishable, or (subexponentially) secure, hard, or indistinguishable if the appropriate  $\epsilon$ -indistinguishability or (subexponentially) indistinguishability holds.

REMARK 2.1 (ON SECURITY LEVELS). We denote by  $\lambda$  the global security parameter and set all other parameters as functions of  $\lambda$ . The security of different assumptions, DLIN over symmetric bilinear groups, LWE, LPN, and PRG in NC<sup>0</sup> depends on their own parameters, namely the order p of the bilinear pairing group, the dimension of the LWE secrets, the dimension of the LPN secrets, and the length of the PRG seeds, and thus are indirectly related to the global security parameter  $\lambda$ . For our FE construction, we need all assumptions to be polynomially hard (in  $\lambda$ ), and for our iO construction, we need all assumptions to be sub-exponentially hard (in  $\lambda$ ).

Indistinguishability Obfuscation. We now define our object of interest, Indistinguishability Obfuscation (*iO*). The notion of indistinguishability obfuscation (*iO*), first conceived by Barak et al. [20], guarantees that the obfuscation of two circuits are computationally indistinguishable as long as they both are equivalent circuits, i.e., the output of both the circuits are the same on every input. Formally,

Definition 2.2 (Indistinguishability Obfuscator (iO) for Circuits). A uniform PPT algorithm iO is called a  $\gamma$ -secure indistinguishability obfuscator for polynomial-sized circuits if the following holds:

• **Completeness:** For every  $\lambda \in \mathbb{N}$ , every circuit *C* with input length *n*, every input  $x \in \{0, 1\}^n$ , we have that

$$\Pr\left[C'(x) = C(x) : C' \leftarrow iO(1^{\lambda}, C)\right] = 1.$$

• Indistinguishability: For every two ensembles  $\{C_{0,\lambda}\}$  and  $\{C_{1,\lambda}\}$  of polynomial-sized circuits that have the same size, input length, and output length, and are functionally equivalent, that is,  $\forall \lambda$ ,  $C_{0,\lambda}(x) = C_{1,\lambda}(x)$  for every input *x*, the following distributions are polynomially indistinguishable.

$$\{iO(1^{\lambda}, C_{0,\lambda})\}$$
  $\{iO(1^{\lambda}, C_{1,\lambda})\}$ 

LPN over Fields Assumption. In this work, we use the LPN assumption over a large field. This assumption has been used in a various works (see for example, [11, 14, 22, 35, 36, 63, 73, 88]). We adopt the following definition from [35]. We use the assumption with error rate  $\ell^{-\delta}$ , for any constant  $\delta > 0$ , where  $\ell$  is the dimension of the LPN secret.

We set up some notation for the definition below. Let p be any prime modulus. We define the distribution  $\mathcal{D}_r(p)$  as the distribution that outputs 0 with probability 1 - r and a random element from  $\mathbb{Z}_p$  with the remaining probability. Below we define  $\delta$ -LPN<sub>F</sub> Assumption [11, 27, 35, 88].

Definition 2.3 ( $\delta$ -LPN<sub>F</sub> Assumption). Let  $\lambda$  be the security parameter. We denote positive integer dimension  $\ell = \ell(\lambda)$ , noise rate  $r = r(\ell) \in (0, 1]$ , and positive integer sample complexity  $n = n(\ell)$ .

We say that the  $\delta$ -LPN<sub>F</sub> Assumption is  $\gamma$ -hard if the following holds:

For any constant  $\eta_p > 0$ , any function  $p : \mathbb{N} \to \mathbb{N}$  s.t., for every  $\lambda \in \mathbb{N}$ ,  $p(\lambda)$  is a prime of  $k^{\eta_p}$  bits, any constants  $\eta_\ell \ge 1$ , and  $\eta_n > 0$ , we set  $p = p(\lambda)$ ,  $\ell = \ell(\lambda) = \lambda^{\eta_\ell}$ ,  $n = n(\ell) = \ell^{\eta_n}$ , and  $r = r(\ell) = \ell^{-\delta}$ , and we require that the following two distributions are  $\gamma$ -indistinguishable:

$$\left\{ \left( \boldsymbol{A}, \boldsymbol{b} = \boldsymbol{s} \cdot \boldsymbol{A} + \boldsymbol{e} \right) \mid \boldsymbol{A} \leftarrow \mathbb{Z}_p^{\ell \times n}, \ \boldsymbol{s} \leftarrow \mathbb{Z}_p^{1 \times \ell}, \ \boldsymbol{e} \leftarrow \mathcal{D}_r^{1 \times n}(p) \right\}$$
$$\left\{ \left( \boldsymbol{A}, \boldsymbol{u} \right) \mid \boldsymbol{A} \leftarrow \mathbb{Z}_p^{\ell \times n}, \ \boldsymbol{u} \leftarrow \mathbb{Z}_p^{1 \times n} \right\}$$

We refer the reader to [35, 37] for a comprehensive discussion of the history and security of this assumption.

#### 3 DEFINITION OF STRUCTURED-SEED PRG

Definition 3.1 (Syntax of Structured-Seed Pseudo-Random Generators (sPRG)). Let  $\tau$  be a positive constant. A structured-seed Boolean PRG, sPRG, with stretch  $\tau$  that maps  $(n \cdot \text{poly}(\lambda))$ -bit binary strings into  $(m = n^{\tau})$ -bit strings, where poly is a fixed polynomial, is defined by the following PPT algorithms:

- IdSamp $(1^{\lambda}, 1^{n})$  samples a function index *I*.
- SdSamp(*I*) jointly samples two binary strings, a public seed and a private seed, sd = (*P*, *S*). The combined length of these strings is  $n \cdot \text{poly}(\lambda)$ .
- Eval(I, sd) computes a string in  $\{0, 1\}^m$ .

REMARK 3.1 (POLYNOMIAL STRETCH.). We say that an sPRG has polynomial stretch if  $\tau > 1$  for some constant  $\tau$ .

REMARK 3.2 (ON poly( $\lambda$ ) MULTIPLICATIVE FACTOR IN THE SEED LENGTH.). As opposed to a standard Boolean PRG definition where the length of the output is set to be  $n^{\tau}$  where n is the seed length, we allow the length of the seed to increase multiplicatively by a fixed polynomial poly in a parameter  $\lambda$ . Looking ahead, one should view n as an arbitrary large polynomial in  $\lambda$ , and hence sPRG will be expanding in length.

*Definition 3.2 (Security of sPRG).* A structured-seed Boolean PRG, sPRG, satisfies

 $\gamma$ -**pseudorandomness:** For every integer  $n = n(\lambda) \ge \lambda$ , the following distributions are  $\gamma$  indistinguishable.

$${I, P, Eval(I, P)}$$
  
 ${I, P, r},$ 

where  $I \leftarrow \text{IdSamp}(1^{\lambda}, 1^{n})$ , sd  $\leftarrow \text{SdSamp}(I)$  and  $r \leftarrow$  $\{0,1\}^{m(n)}$ 

*Definition 3.3 (Complexity and degree of* sPRG). Let  $d \in \mathbb{N}$ , let  $\lambda \in$  $\mathbb{N}$  and  $n = n(\lambda)$  be arbitrary positive polynomial in  $\lambda$ , and  $p = p(\lambda)$ denote a prime modulus which is an efficiently computable function in  $\lambda$ . Let  $\mathbb{C}$  be a complexity class. A sPRG has complexity  $\mathbb{C}$  in the public seed and degree d in private seed over  $\mathbb{Z}_p$ , denoted as, sPRG  $\in$ ( $\mathbb{C}$ , deg d), if for every I in the support of IdSamp $(1^{\lambda}, 1^{n})$ , there exists an algorithm  $Process_I$  in  $\mathbb{C}$  and an m(n)-tuple of polynomials  $Q_I$  that can be efficiently generated from I, such that for all sd in the support of SdSamp(I), it holds that:

$$Eval(I, sd) = Q_I(P, S) \text{ over } \mathbb{Z}_p, P = Process_I(P)$$

where  $Q_I$  has degree 1 in  $\overline{P}$  and degree d in S.

We remark that the above definition generalizes the standard notion of families of PRGs in two aspects: 1) the seed consists of a public part and a private part, jointly sampled and arbitrarily correlated, and 2) the seed may not be uniform. Therefore, we obtain the standard notion as a special case.

Definition 3.4 (Pseudo-Random Generators, degree, and locality). A (uniform-seed) Boolean PRG (PRG) is an sPRG with a seed sampling algorithm SdSamp(I) that outputs a public seed *P* that is an empty string and a uniformly random private seed  $S \leftarrow \{0, 1\}^n$ , where the polynomial poly is fixed to be 1.

Let  $d, c \in \mathbb{N}$ . The PRG has multilinear degree d if for every  $n \in \mathbb{N}$ and *I* in the support of  $IdSamp(1^n)$ , we have that Eval(I, sd) can be written as an m(n)-tuple of degree-d polynomials over  $\mathbb{Z}$  in S. It has constant locality *c* if for every  $n \in \mathbb{N}$  and *I* in the support of  $IdSamp(1^n)$ , every output bit of Eval(I, sd) depends on at most *c* bits of S.

#### CONSTRUCTION OF STRUCTURED SEED 4 PRG

In this section, we construct a family of structured-seed PRGs whose evaluation has degree 2 in the private seed, and constant degree in the public seed.

THEOREM 4.1. Let  $\lambda$  be the security parameter. Let  $d \in \mathbb{N}, \delta \in$  $(0, 1), \tau > 1$  be arbitrary constants.

Assuming the following:

- the existence of a constant locality Boolean PRG with stretch  $\tau > 1$  and multilinear degree d over  $\mathbb{Z}$ , and,
- $\delta$ -LPN<sub>F</sub>-assumption holds,

Then, there exists an sPRG with polynomial stretch in (deg d, deg 2). Additionally, if both assumptions are subexponentially secure, then, sPRG is subexponentially secure.

Technical Overview. Let PRG = (IdSamp, Eval) be the Boolean PRG with multilinear degree d and stretch  $\tau$ . Our sPRG will simply evaluate PRG on an input  $\sigma \in \{0,1\}^n$  and return its output  $\boldsymbol{y} \in$  $\{0,1\}^m$  where  $m = n^{\tau}$ . The challenge stems from the fact that the evaluation algorithm  $\text{Eval}_I(\sigma)$  of PRG has degree *d* in its private seed  $\sigma$ , but the evaluation algorithm  $Eval'_{I}(P, S)$  of sPRG can only have degree 2 in the private seed S. To resolve this, we pre-process  $\sigma$  into appropriate public and private seeds (*P*, *S*) and leverage the LPN over  $\mathbb{Z}_p$  assumption to show that the seed is hidden.

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Towards this, sPRG uses a  $\lambda$ -bit prime *p* and "encrypts" the seed  $\sigma$  using LPN samples over  $\mathbb{Z}_p$  as follows:

Sample: 
$$\mathbf{A} \leftarrow \mathbb{Z}_p^{\ell \times n}$$
,  $\mathbf{s} \leftarrow \mathbb{Z}_p^{1 \times \ell}$ ,  $\mathbf{e} \leftarrow \mathcal{D}_r^{1 \times n}(p)$ 

Add to the function index I': **A** 

Add to public seed  $P: \mathbf{b} = \mathbf{sA} + \mathbf{e} + \boldsymbol{\sigma}$ 

It follows directly from the LPN over  $\mathbb{Z}_p$  assumption that  $(\mathbf{A}, \boldsymbol{b})$  is pseudorandom and hides  $\sigma$ . Furthermore, due to the sparsity of LPN noises, the vector  $\boldsymbol{\sigma} + \boldsymbol{e}$  differs from  $\boldsymbol{\sigma}$  only at a  $\ell^{-\delta}$  fraction of components - thus it is a sparsely erroneous version of the seed. Given such "encryption", by applying previous techniques

[6, 68, 89, 90] that work essentially by "replacing monomials" (previous works replace monomials in the PRG seed with polynomials in the LWE secret, and we here replace the monomials in the erroneous seed with polynomials in the LPN secret) we can compute PRG on the erroneous seed  $\sigma + e$  via a polynomial map  $G^{(1)} = (G_1^{(1)}, \dots, G_m^{(1)})$  that depends on A and has degree d in the public component **b** and only degree 2 in all possible degree  $\left\lceil \frac{d}{2} \right\rceil$ monomials in **s**. More precisely, each polynomial  $G_i^{(1)}$  denotes the polynomial computing the  $j^{th}$  output coordinate and is defined as follows:

$$G^{(1)}(\boldsymbol{b}, (\overline{\boldsymbol{s}}^{\otimes |\frac{\boldsymbol{a}}{2}|})) \coloneqq \operatorname{Eval}_{I}(\boldsymbol{b} - \boldsymbol{s} \mathbf{A}) = \operatorname{Eval}_{I}(\boldsymbol{\sigma} + \boldsymbol{e}), \ \overline{\boldsymbol{s}} = \boldsymbol{s}||1 \ (1)$$

where for any vector v, we denote by  $v^{\otimes k}$  the tensoring of the vector v with itself k times, yielding a vector of dimension dim $(v)^k$ . In particular, observe that by setting the dimension  $\ell$  of secret s sufficiently small, the polynomial map  $G^{(1)}$  can be expanding; namely, set  $\ell(n)$  so that  $n' = \dim((b, \overline{s}^{\otimes \lceil \frac{d}{2} \rceil})) = (n + \ell^{\lceil \frac{d}{2} \rceil}) \ll m =$  $n^{\tau}$ . In this overview, we compare the number of output bits *m* with the number of field elements in the seed of sPRG because we will choose a  $\lambda$ -bit modulus p. Thus, the seed length is  $n'\lambda$ , with  $n' \ll m$ , satisfying the notion of polynomial stretch for sPRG (in particular, we have polynomial expansion as  $\lambda$  is asymptotically smaller than n' and m).

However, a new problem arises: even though the degree fits,  $G^{(1)}$  only evaluates an erroneous output  $\mathbf{y}' = \text{Eval}_I(\boldsymbol{\sigma} + \boldsymbol{e})$ , but we want to obtain the correct output  $\boldsymbol{y} = \text{Eval}_{I}(\boldsymbol{\sigma})$ . To correct errors, we further modify the polynomial and include more preprocessed information in the private seeds. Our key observation is the following: Because LPN noises are sparse, and because Eval<sub>I</sub> has only constant locality, only a few outputs depend on erroneous seed locations. We refer to them as bad outputs and let BAD denote the set of their indices. The probability that a single output bit is bad is bounded by  $\frac{O(1)}{\ell^{\delta}}$ . By a simple Markov argument, the number of bad outputs is bounded by  $\Gamma = \frac{m \log n}{\ell^{\delta}}$  with probability 1 - o(1). Leveraging this sparsity, we devise a method for correcting the bad outputs below.

We describe a sequence of ideas that lead to the final correction method. To start with, we describe two wrong ideas that illustrate the challenges we will overcome. Then, we show how to correct bad outputs in three simple cases that are increasingly less restrictive, which lead to our final solution.

Challenges to Correction:

Indistinguishability Obfuscation from Well-Founded Assumptions

- The first wrong idea is correcting by adding the difference Corr = y - y' between the correct and erroneous outputs, y = Eval<sub>I</sub>(σ) and y' = Eval<sub>I</sub>(σ + e); we refer to Corr as the *correction vector*. To obtain the correct output, evaluation can compute the polynomial map G<sup>(1)</sup>(b, (s<sup>∞</sup>[<sup>d</sup>/<sub>2</sub>])) + Corr. The problem is that Corr must be included in the seed, but it is as long as the output and would kill expansion.
- To fix expansion, the second wrong idea is adding correction only for bad outputs, so that the seed only stores non-zero entries in Corr, which is short (bounded by Γ elements). More precisely, the *j*'th output can be computed as G<sup>(1)</sup><sub>j</sub>(**b**,

 $(\overline{s}^{\otimes \lfloor \frac{d}{2} \rceil}))$ +Corr<sub>*j*</sub> if output *j* is bad and without adding Corr<sub>*j*</sub> otherwise. This fixes expansion, but now the evaluation polynomial depends on the location of bad outputs. If these locations are included in public information, this would leak information of the location of LPN noises, and jeopardize security. If on the other hand the locations are included in the private seed, then it is unclear how to maintain the requirement that the polynomial map computes only a degree 2 polynomial in the private seed.

These two wrong ideas illustrate the first difficulty: The tension between the expansion and security of our sPRG. Our construction takes care of both, by *compressing* the correction vector Corr to be polynomially shorter than the output and stored in the seed, and *expanding* it back during evaluation in a way that is oblivious of the location of bad output bits. This is possible thanks to the sparsity of the correction vector and the allowed degree 2 computation on the private seed. The second challenge stems from the fact that with noticeable probability, the number of bad outputs is not bounded by  $\Gamma$ . We will overcome this via parallel repetition: Instead of having one LPN encryption of the seed, we will have  $\lambda$  copies  $\boldsymbol{b}^{(k)} = \boldsymbol{s}^{(k)} \mathbf{A} + \boldsymbol{e}^{(k)} + \boldsymbol{\sigma}$ , so that with overwhelming probability at least one copy has sparse bad outputs. This copy can be used to compute the output of the PRG. However, we must be careful to hide which copies have sparse bad outputs.

Simple Case 1: Much fewer than  $\sqrt{m}$  bad outputs. Suppose hypothetically that the number of bad outputs is bounded by z which is much smaller than  $\sqrt{m}$ . Thus, if we convert Corr into a  $\sqrt{m} \times \sqrt{m}$  matrix<sup>4</sup>, it has low rank z. We can then factorize Corr into two matrixes U and V of dimensions  $\sqrt{m} \times z$  and  $z \times \sqrt{m}$  respectively, such that Corr = UV, and compute the correct output as follows:

$$\forall j \in [m], \ G_j^{(2)}(\boldsymbol{b}, \ (\overline{\boldsymbol{s}}^{\otimes \lceil \frac{d}{2} \rceil}, \mathbf{U}, \mathbf{V})) = G_j^{(1)}(\boldsymbol{b}, \ (\overline{\boldsymbol{s}}^{\otimes \lceil \frac{d}{2} \rceil})) + (\mathbf{U}\mathbf{V})_{k_j, l_j}$$

where  $(k_j, l_j)$  is the corresponding index of the output bit j, in the  $\sqrt{m} \times \sqrt{m}$  matrix. When  $z \ll \sqrt{m}$ , the matrices **U**, **V** have  $2z\sqrt{m}$  field elements, which is polynomially smaller than  $m = n^{\tau}$ . As such,  $G^{(2)}$  is expanding.

Moreover, observe that  $G^{(2)}$  has only degree 2 in the private seed and is completely oblivious of where the bad outputs are. Simple Case 2: Up to  $\Gamma$  bad outputs, which are however evenly spread. The above method however cannot handle more than  $\sqrt{m}$ bad outputs, whereas the actual number of bad outputs can be up to  $\Gamma = m(\log n)/t^{\delta}$ , much larger than  $\sqrt{m}$  since  $\delta$  is an arbitrarily small constant. Consider another hypothetical case where the bad outputs are evenly spread in the following sense: Suppose that if we divide the matrix Corr into  $m/\ell^{\delta}$  blocks, each of dimension  $\ell^{\delta/2} \times \ell^{\delta/2}$ , there are at most log *n* bad outputs in each block. In this case, we can "compress" each block of Corr separately using the idea from case 1. More specifically, for every block  $i \in [m/\ell^{\delta}]$ , we factor it into  $U_i \mathbf{V}_i$ , with dimensions  $\ell^{\delta/2} \times \log n$  and  $\log n \times \ell^{\delta/2}$  respectively, and correct bad outputs as follows:

$$\begin{aligned} \forall j \in [m], \qquad & G_j^{(2)}\left(\boldsymbol{b}, \left(\overline{\boldsymbol{s}}^{\otimes \left\lceil \frac{d}{2} \right\rceil}, \left(\mathbf{U}_i, \mathbf{V}_i\right)_{i \in \left\lceil \frac{m}{\ell^{\delta}} \right\rceil}\right)\right) \\ &= & G_j^{(1)}\left(\boldsymbol{b}, \left(\overline{\boldsymbol{s}}^{\otimes \left\lceil \frac{d}{2} \right\rceil}\right)\right) + (\mathbf{U}_{i_j}\mathbf{V}_{i_j})_{k_j, l_j}, \end{aligned}$$

where  $i_j$  is the block that output j belongs to, and  $(k_j, l_j) \in [\ell^{\delta/2}] \times [\ell^{\delta/2}]$  is its index within this block. We observe that  $G^{(2)}$  is expanding, since each matrix  $\mathbf{U}_i$  or  $\mathbf{V}_i$  has  $\ell^{\delta/2} \log n$  field elements, and the total number of elements is  $\ell^{\delta/2} \log n \cdot \frac{m}{\ell^{\delta}}$  which is polynomially smaller than m as long as  $\delta$  is positive and m is polynomially related to  $\ell$ . Moreover,  $G^{(2)}$  is oblivious of the location of bad outputs just as in case 1.

At this point, it is tempting to wish that bad outputs must be evenly spread given that the LPN noises occur at random locations. This is, however, not true because the input-output dependency graph of PRG is arbitrary, and the location of bad outputs are correlated. Consider the example that the PRG output dependency graph is such that for the first  $K = \ell^{\delta} \cdot \log^2 n$  seed bits, the *i*'th seed bit impacts all the output positions in the *i*'th block. In this case, with overwhelming probability, one of the first *K* seed bits would be errorneous, and hence at least one block will be completely corrupt. Simple Case 3: Up to  $\Gamma$  bad outputs. To overcome this, our next idea is to "force" the even spreading of the bad outputs, by assigning output bits randomly into *B* buckets, and then compress the correction vector corresponding to each bucket.

- **Step 1: Randomly assign outputs.** We assign the outputs into *B* buckets, via a random mapping  $\phi_{bkt} : [m] \rightarrow [B]$ . The number of buckets is set to  $B = mt/\ell^{\delta}$  where *t* is a *slack parameter* set to  $\lambda$ . By a Chernoff-style argument, we can show that each bucket contains at most  $t^2\ell^{\delta}$  output bits, and at most *t* of them are bad, except with negligible probability in  $t = \lambda$ . As such, bad outputs are evenly spread among a small number of not-so-large buckets.
- Step 2: Compress the buckets. Next, we organize each bucket *i* into a matrix  $\mathbf{M}_i$  of dimension  $t\ell^{\delta/2} \times t\ell^{\delta/2}$  and then compute its factorization  $\mathbf{M}_i = \mathbf{U}_i \mathbf{V}_i$  with respect to matrices of dimensions  $t\ell^{\delta/2} \times t$  and  $t \times t\ell^{\delta/2}$  respectively. To form matrix  $\mathbf{M}_i$ , we use another mapping  $\phi_{\text{ind}} : [m] \rightarrow$  $[t\ell^{\delta/2}] \times [t\ell^{\delta/2}]$  to assign each output bit *j* to an index  $(k_j, l_j)$  in the matrix of the bucket  $i_j$  it is assigned to. This assignment must guarantee that no two output bits in the same bucket (assigned according to  $\phi_{\text{bkt}}$ ) have the same index; other than that, it can be arbitrary.  $(\mathbf{M}_i)_{k,l}$  is set to Corr<sub>j</sub> if there is *j* such that  $\phi_{\text{bkt}}(j) = i$  and  $\phi_{\text{ind}}(j) = (k, l)$ , and set to 0 if no such *j* exists. Since every matrix  $\mathbf{M}_i$  has at most *t* non-zero entries, we can factor them and compute

 $<sup>^4\</sup>mathrm{Any}$  injective mapping from a vector to a matrix that is efficient to compute and invert will do.

the correct output as:

$$\forall j \in [m], G_j^{(2)} \left( \boldsymbol{b} , \underbrace{\left( \overline{\boldsymbol{s}}^{\otimes \left\lceil \frac{d}{2} \right\rceil}, (\mathbf{U}_i, \mathbf{V}_i)_{i \in [B]} \right)}_{S} \right)$$
$$= G_j^{(1)} \left( \boldsymbol{b} , (\overline{\boldsymbol{s}}^{\otimes \left\lceil \frac{d}{2} \right\rceil}) \right) + (\mathbf{U}_{\phi_{\text{bkt}}(j)} \cdot \mathbf{V}_{\phi_{\text{bkt}}(j)})_{\phi_{\text{ind}}(j)}$$

 $G^{(2)}$  is expanding, because the number of field elements in  $\mathbf{U}_i$ 's and  $\mathbf{V}_i$ 's are much smaller than *m*, namely:  $2t^2 t^{\delta/2}B = O(m\lambda^3/t^{\delta/2}) \ll m$  (recall that  $t = \lambda$ ), if t is a large enough polynomial in  $\lambda$ . Note that it is important that the assignments  $\phi_{\text{bkt}}$  and  $\phi_{\text{ind}}$  are not included in the seed as their description is as long as the output. Fortunately, they are reusable and can be included in the function index  $I' = (I, \mathbf{A}, \phi_{\text{bkt}}, \phi_{\text{ind}})$ .

Final Case: More than  $\Gamma$  bad outputs with noticable probability. Unfortunately, the number of bad output bits are only bounded by  $\Gamma$ with probability noticably away from 1. This is inevitable: Consider the example that every output bit of PRG depends on the first seed bit, and with inverse polynomial probability  $\frac{1}{\epsilon\delta}$ , it is erroneous and so are all outputs. When bad outputs are not sparse, our analysis above does not apply, in particular, some bucket may be assigned more than  $t = \lambda$  bad outputs. We resort to parallel repetition to deal with this. Instead of generating a single LPN encryption of the seed *σ*, generate λ instances,  $\{ \mathbf{b}^{(k)} = \mathbf{s}^{(k)}\mathbf{A} + \boldsymbol{\sigma} + \mathbf{e}^{(k)} \}_{k \in [\lambda]}$ . For each instance *k*, we can compute the corresponding correction vector Corr<sup>(k)</sup> and set a flag  $flag^{(k)} = 1$  if k is the *smallest* index such that the number of bad outputs is bounded by  $\Gamma$  (flag<sup>(k)</sup> = 0 otherwise). By that fact that each instance satisfies the desired sparsity with high probability and is independent, with all but exponentially small probability, some flag is set  $flag^{(k^{\star})} = 1$  and  $k^{\star}$  is unique. If no flag is set, we set err = 1, which occurs with exponentially small probability (err = 0 otherwise). For the special  $k^{\star}$ , we can compress its correction vector  $\operatorname{Corr}^{k^*}$  into  $(\mathbf{U}_i^{(k^*)}, \mathbf{V}_i^{(k^*)})_{i \in [B]}$  and set the secret seed  $S^{(k^{\star})}$  as before, which satisfies:

$$G^{(2)}\left(\boldsymbol{b}^{(k^{\star})}, \underbrace{\left(\left(\overline{\mathbf{s}}^{(k^{\star})}\right)^{\otimes \left\lceil \frac{d}{2} \right\rceil}, \left(\mathbf{U}_{i}^{(k^{\star})}, \mathbf{V}_{i}^{(k^{\star})}\right)_{i \in [B]}\right)}_{S^{(k^{\star})}}\right) = \operatorname{Eval}_{I}(\boldsymbol{\sigma}) .$$

The tricky part is that we must hide the value of  $k^*$  as it leaks a single bit of information about the noise vector  $e^{(k^*)}$ , namely, it leads to sparse bad outputs. To do so, we set  $S^{(k^*)} = \mathbf{0}$  for every other instance  $k \neq k^*$ , and aim to compute the boxed part the in following equation.

$$G^{(3)}\left(\underbrace{\left\{\left\{\boldsymbol{b}^{(k)}\right\}_{k}, \operatorname{err}\right\}}_{P'}, \underbrace{\left\{\operatorname{flag}^{(k)}, S^{(k)}, \operatorname{flag}^{(k)} \cdot S^{(k)}\right\}_{k}}_{S'}\right)$$
$$\coloneqq \underbrace{\left[\left(1 - \operatorname{err}\right)\left(\sum_{k \in [\lambda]} \operatorname{flag}^{(k)} \cdot G^{(2)}(\boldsymbol{b}^{(k^{\star})}, S^{(k^{\star})})\right)\right]}_{= (1 - \operatorname{err})\operatorname{Eval}_{I}(\boldsymbol{\sigma}).$$

The last equality holds since when err = 0, there is a unique  $k^*$  for which the flag is 1. The boxed part can be computed by a polynomial

 $G^{(3)}$  with degree d + 1 in the public seed P' and degree 2 in the private seed S', since the multiplication between flag<sup>(k)</sup> and  $S^{(k)}$  is pre-computed in S'. Furthermore, we observe that comparing with polynomial  $G^{(2)}$ , the length of the seed for  $G^{(3)}$  has increased by a factor of  $O(\lambda)$ , which does not hurt expansion.

For security, observe that the polynomial map  $G^{(3)}$  is oblivious of LPN noises in all instances, while the public seed leaks the error flag err, which is 1 with only exponentially small probability. Therefore, by the LPN over  $\mathbb{Z}_p$  assumption, the seed  $\sigma$  of PRG is hidden in all  $\lambda$  instances and the security of PRG ensures that the output is pseudorandom. We now proceed to the formal construction and proof.

*Construction.* Assume the premise of the theorem. Let (IdSamp, Eval) be the function index sampling algorithm and evaluation algorithm for the PRG. Recall that its seed consists of only a private seed sampled uniformly at random.

We first introduce and recall some notation. The construction is parameterized by

- the security parameter  $\lambda$ ,
- the seed length parameter n' of the sPRG and output length  $m' = (n')^{\tau'}$ ,
- the stretch Γ and degree *d* of PRG, where Γ can be an arbitrary positive constant greater than 1, and *d* can be an arbitrary positive integer,
- the input length parameter n and output length m = n<sup>t</sup> of the PRG (as we see below, by construction, m' = m and n' is larger than n, but still sublinear in m'),
- the LPN secret dimension  $\ell = n^{1/\lceil d/2 \rceil}$  and modulus *p*, which is a  $\lambda$  bit prime,
- a threshold  $\Gamma = m \cdot \log n/\ell^{\delta}$  of the number of bad outputs that can be tolerated,
- a slack parameter *t* used for bounding the capacity of each bucket and number of bad outputs in each bucket, which is set to *t* = λ,
- a parameter  $B = m \cdot t/\ell^{\delta}$  that indicates the number of buckets used, and
- a parameter  $c = t^2 t^{\delta}$  that indicates the capacity of each bucket.
- $I' \leftarrow \text{IdSamp}'(1^{\lambda}, 1^{n'})$ : (Note that the PRG seed length *n* below is an efficiently computable polynomial in *n'*, and can be inferred from the next seed sampling algorithm. See Claim 4.1 for the exact relationship between *n* and *n'*.)

Sample  $I \leftarrow \mathsf{IdSamp}(1^{\lambda}, 1^n)$  and  $\mathbf{A} \leftarrow \mathbb{Z}_p^{\ell \times n}$ . Prepare two functions  $\boldsymbol{\phi} = (\phi_{\mathsf{bkt}}, \phi_{\mathsf{ind}})$  as follows:

- Sample a random function  $\phi_{bkt} : [m] \rightarrow [B]$  mapping every output location to one of *B* buckets. Let  $\phi_{bkt}^{-1}(i)$  for  $i \in [B]$  denote the set of preimages of *i* through  $\phi_{bkt}$ . This set contains all outputs assigned to the bucket *i*.
- Prepare  $\phi_{\text{ind}} : [m] \to [\sqrt{c}] \times [\sqrt{c}]$  in two cases:
  - If some bucket exceeds capacity, that is, there exists  $i \in [B]$  such that  $|\phi_{bkt}^{-1}(i)| > c$ , set  $\phi_{ind}$  to be a constant function always outputting (1, 1).
  - Otherwise if all buckets are under capacity, for every index  $j \in [m]$ ,  $\phi_{ind}$  maps j to a pair of indexes  $(k_j, l_j) \in$

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 $[\sqrt{c}] \times [\sqrt{c}]$ , under the constraint that two distinct output bits  $j_1 \neq j_2$  that are mapped into the same bucket  $\phi_{\text{bkt}}(j_1) = \phi_{\text{bkt}}(j_2)$  must have distinct pairs of indices  $\phi_{\text{ind}}(j_1) \neq \phi_{\text{ind}}(j_2)$ .

Output  $I' = (I, \boldsymbol{\phi} = (\phi_{bkt}, \phi_{ind}), \mathbf{A}).$ 

- sd  $\leftarrow$  SdSamp'(*I*'): Generate the seed as follows:
  - Sample a PRG seed  $\sigma \leftarrow \{0, 1\}^n$ .
  - For every  $k \in [\lambda]$ , run (flag<sup>(k)</sup>,  $P^{(k)}$ ,  $S^{(k)}$ )  $\leftarrow$  1SdSamp( $I', \sigma$ ), where the 1SdSamp algorithm is described below.
  - Set err = 1 − max<sub>k∈[λ]</sub>(flag<sup>(k)</sup>). That is err is 1 if no flag is set, and otherwise, it is zero.
  - If err = 0, let k\* = min{k : flag<sup>(k)</sup> = 1} and for every k > k\*, overwrite flag<sup>k</sup> = 0. (This ensure that there is a unique k for which the flag is set to 1.)
  - Set  $P' = (\{P^{(k)}\}_{k \in [\lambda]}, \operatorname{err})$  and  $S' = (\{\operatorname{flag}^{(k)}, S^{(k)}, \operatorname{flag}^{(k)}, S^{(k)}\}_{k \in [\lambda]})$ .
  - Output sd = (P', S'),

**Sampling Algorithm** 1SdSamp( $I', \sigma$ ) **Input:** sPRG index  $I' = (I, \phi = (\phi_{bkt}, \phi_{ind}), A)$  and random seed  $\sigma$ .

**Output:** A flag flag  $\in \{0, 1\}$ , and a pair of public and private seeds (P, S).

• Prepare samples of LPN over  $\mathbb{Z}_p$ : Sample  $s \leftarrow \mathbb{Z}_p^{1 \times \ell}$ ,  $e \leftarrow \mathcal{D}_r^{1 \times n}(p)$ , and set

$$b = s\mathbf{A} + \sigma + e \; .$$

Find indices *i* ∈ [*n*] of seed bits where *σ* + *e* and *σ* differ, which are exactly these indices where *e* is not 0, and define:

 $\mathsf{ERR} = \{i \mid \sigma_i + e_i \neq \sigma_i\} = \{i \mid e_i \neq 0\} \ .$ 

We say a seed index *i* is *erroneous* if  $i \in ERR$ . Since LPN noise is sparse, errors are sparse.

 Find indices *j* ∈ [*m*] of outputs that depend on one or more erroneous seed indices. Let Vars<sub>*j*</sub> denote the indices of seed bits that the *j*'th output of Eval<sub>*I*</sub> depends on. Define:

 $\mathsf{BAD} = \left\{ j \mid |\mathsf{Vars}_j \cap \mathsf{ERR}| \ge 1 \right\} \;.$ 

We say an output index j is bad if  $j \in BAD$ , and good otherwise.

- Set flag = 0 if
- (1) Too many bad output bits:  $|BAD| > \Gamma$ ,
- (2) or Some bucket exceeds capacity:  $\exists i \in [B], |\phi_{bkt}^{-1}(i)| > c,$
- (3) or Some bucket contains too many bad outputs:  $\exists i \in [B], |\phi_{bkt}^{-1}(i) \cap BAD| > t.$
- Otherwise, set flag = 1. • Compute the outputs of PRG on input the correct
- seed and the erroneous seed,  $y = \text{Eval}_I(\sigma)$  and  $y' = \text{Eval}_I(\sigma + e)$ . Set the correction velocity of Correction V = V = y y'.
- Construct matrices  $\mathbf{M}_1, \ldots, \mathbf{M}_B$ , by setting

$$\forall j \in [m], \left(\mathbf{M}_{\phi_{\mathsf{bkt}}(j)}\right)_{\phi_{\mathsf{ind}}(j)} = \operatorname{Corr}_{j}$$

Every other entry is set to 0.

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- "Compress" matrices **M**<sub>1</sub>,..., **M**<sub>B</sub> as follows:
  - − If flag = 1, for every  $i \in [B]$  compute factorization

$$\mathbf{M}_i = \mathbf{U}_i \mathbf{V}_i, \qquad \mathbf{U}_i \in \mathbb{Z}_p^{\sqrt{c} \times t}, \ \mathbf{V}_i \in \mathbb{Z}_p^{t \times \sqrt{c}}$$

- This factorization exists because when flag = 1, condition 3 above implies that each  $M_i$  has at most *t* nonzero entries, and hence rank at most *t*.
- If flag = 0, for every  $i \in [B]$ , set  $\mathbf{U}_i$  and  $\mathbf{V}_i$  to be 0 matrices.
- Set the public seed to P = b.
- Prepare the private seed *S* as follows. Let  $\overline{s} = s || 1$ .

$$S = \left(\overline{\mathbf{s}}^{\otimes \left\lceil \frac{a}{2} \right\rceil}, \{\mathbf{U}_i, \mathbf{V}_i\}_{i \in [B]}\right)$$
(2)

Output (flag, P, S) as  $\mathbb{Z}_p$  elements.

 $y' \leftarrow \text{Eval}'(I', \text{sd})$ : Compute  $y \leftarrow \text{Eval}(I, \sigma)$ , and output  $z = (1 - \text{err}) \cdot y$ . This computation is done via a polynomial map  $G^{(4)}$ , such that,

$$G^{(4)}(P', S') =$$

$$\sum_{k \in [\lambda]} G^{(3)}\left(P^{(k)}, \left(\mathsf{flag}^{(k)}, S^{(k)}, \mathsf{flag}^{(k)} \cdot S^{(k)}\right)\right)$$

$$= \sum_{k \in [\lambda]} \mathsf{flag}^{(k)} \cdot \mathsf{Eval}_{I}(\sigma) = (1 - \mathsf{err}) \cdot \boldsymbol{y}.$$

where the second equality follows from the fact that when err = 0, there is a unique  $k \in [\lambda]$  such that  $\operatorname{flag}^{(k)} = 1$ , and when err = 1, all flags are zero. Furthermore, the polynomial mapping  $G^{(3)}$  computing  $\operatorname{flag}^{(k)} \cdot \operatorname{Eval}_I(\sigma)$  is described below.  $G^{(3)}$  has constant degree d in the public seed  $P^{(k)}$  and only degree 2 in the private seed ( $\operatorname{flag}^{(k)}, S^{(k)}, \operatorname{flag}^{(k)} \cdot S^{(k)}$ ). Hence  $G^{(4)}$  also has constant degree d in P' and degree 2 in S'.

**The Polynomial**  $G^{(3)}$  (P, (flag, S, flag  $\cdot S$ )) **Constant:**  $I' = (I, \phi = (\phi_{bkt}, \phi_{ind}), \mathbf{A})$  **Input:**  $P = \mathbf{b} = \mathbf{s}\mathbf{A} + \boldsymbol{\sigma} + \mathbf{e}, S = (\overline{\mathbf{s}}^{\otimes \lceil \frac{d}{2} \rceil}, \{\mathbf{U}_i, \mathbf{V}_i\}_{i \in [B]})$ **Output:** flag  $\cdot$  Eval $_I(\boldsymbol{\sigma})$ .

We describe  $G^{(3)}$  using intermediate polynomials  $G^{(1)}$ and  $G^{(2)}$ .

• Every output bit of Eval is a linear combination of degree *d* monomials (without loss of generality, assume that all monomials have exactly degree *d* which can be done by including 1 in the seed *σ*).

**Notation** Let us introduce some notation for monomials. A monomial *h* on a vector *a* is represented by the set of indices  $h = \{i_1, i_2, \ldots, i_k\}$  of variables used in it. *h* evaluated on *a* is  $\prod_{i \in h} a_i$  if  $h \neq \emptyset$  and 1 otherwise. We will use the notation  $a_h = \prod_{i \in h} a_i$ . We abuse notation to also use a polynomial *g* to denote the set of monomials involved in its computation; hence  $h \in g$  says monomial *h* has a nonzero coefficient in *g*.

With the above notation, we can write Eval as

 $\forall j \in [m], y_j = \operatorname{Eval}_j(\sigma) = L_j((\sigma_h)_{h \in \operatorname{Eval}_j}),$ 

for a linear  $L_j$ .

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 (A, b = sA + x) in the public seed encodes x = σ + e. Therefore, we can compute every monomial x<sub>v</sub> as follows:

$$x_i = \langle \boldsymbol{c}_i, \ \overline{\boldsymbol{s}} \rangle,$$
  
$$x_v = \langle \otimes_{i \in v} \boldsymbol{c}_i, \ \otimes_{i \in v} \overline{\boldsymbol{s}} \rangle,$$

where,  $c_i = -a_i^{\mathrm{T}} || b_i$  and  $a_i$  is the *i*th column of A. (Recall that  $\bigotimes_{i \in v} z_i = z_{i_1} \otimes \cdots \otimes z_{i_k}$  if  $v = \{i_1, \ldots, i_k\}$  and is not empty; otherwise, it equals 1.) Combining with the previous step, we obtain a polynomial mapping  $G^{(1)}(b, S)$  that computes  $\mathrm{Eval}(\sigma + e)$ :

$$G_{j}^{(1)}(\boldsymbol{b}, S) \coloneqq L_{j}\left((\langle \otimes_{i \in v} \boldsymbol{c}_{i}, \otimes_{i \in v} \overline{\boldsymbol{s}} \rangle)_{v \in \mathsf{Eval}_{j}}\right) .$$
(3)

Note that  $G^{(1)}$  implicitly depends on **A** contained in I'. Since all relevant monomials v have degree d, we have that  $G^{(1)}$  has degree at most d in P, and degree 2 in S. The latter follows from the fact that Scontains  $\overline{s}^{\otimes \lceil \frac{d}{2} \rceil}$  (see Equation (1)), and hence  $S \otimes S$ contains all monomials in s of total degrees d. Since only bad outputs depend on erroneous seed bits such that  $\sigma_i + e_i \neq \sigma_i$ , we have that the output of  $G^{(1)}$  agrees with the correct output  $\boldsymbol{y} = \text{Eval}(\sigma)$ on all good output bits.

$$\forall j \notin BAD, Eval_j(\sigma) = G_i^{(1)}(\boldsymbol{b}, S)$$
.

• To further correct bad output bits, we add to *G*<sup>(1)</sup> all the expanded correction vectors as follows:

$$\begin{split} G_j^{(2)}(P,S) &\coloneqq G_j^{(1)}(\boldsymbol{b},S) + \left(\mathbf{U}_{\phi_{\mathsf{bkt}}(j)}\mathbf{V}_{\phi_{\mathsf{bkt}}}(j)\right)_{\phi_{\mathsf{ind}}(j)} \\ &= G_j^{(1)}(\boldsymbol{b},S) + \left(\mathbf{M}_{\phi_{\mathsf{bkt}}(j)}\right)_{\phi_{\mathsf{ind}}(j)} \,. \end{split}$$

We have that  $G^{(2)}$  agrees with the correct output  $\mathbf{y} = \text{Eval}(\boldsymbol{\sigma})$  if flag = 1. This is because under the three conditions for flag = 1, every entry j in the correction vector  $\text{Corr}_j$  is placed at entry  $\left(\mathbf{M}_{\phi_{\text{bkt}}(j)}\right)_{\phi_{\text{ind}}(j)}$ . Adding it back as above produces the correct output.

Observe that the function is quadratic in *S* and degree *d* in the public component of the seed *P*.

• When flag = 0, however, sPRG needs to output all zero. This can be done by simply multiplying flag to the output of  $G^{(2)}$ , giving the final polynomial

$$G^{(3)}(P,S) = \text{flag} \cdot G^{(2)}(P,S)$$
. (4)

At last,  $G^{(3)}$  still has degree *d* in the public seed, and only degree 2 in the private seed, since multiplication between flag and *S* is precomputed.

Analysis of Stretch. We derive a set of constraints, under which sPRG has polynomial stretch. Recall that PRG output length is  $m = n^{\tau}$ , degree *d*, LPN secret dimension  $\ell = n^{1/\lceil d/2 \rceil}$ , modulus *p* a  $\lambda$ -bit prime, and the slack parameter  $t = \lambda$ .

CLAIM 4.1. For the parameters as set in the Construction, sPRG has stretch of  $\tau'$  for some constant  $\tau' > 1$ .

PROOF. Let's start by analyzing the length of the public and private seeds.

• The public seed contains  $P' = (\{b^{(k)}\}_{k \in [\lambda]}, err)$ , where

$$\left| \boldsymbol{b}^{(k)} \right| = O(n \log p) = O(n \cdot \lambda)$$
.

• The private seed S' contains {flag<sup>(k)</sup>,  $S^{(k)}$ , flag<sup>(k)</sup> ·  $S^{(k)}$ }, where each  $S^{(k)}$  contains the following

$$S_1^{(k)} = \left(\overline{\mathbf{s}}^{(k)}\right)^{\otimes \left\lceil \frac{a}{2} \right\rceil}, \qquad S_2^{(k)} = \left\{ \mathbf{U}_i^{(k)}, \mathbf{V}_i^{(k)} \right\}_{i \in [B]}$$

The bit-lengths are:

$$\begin{split} \left| S_1^{(k)} \right| &= (\ell+1)^{\lceil d/2 \rceil} \log p \\ &= O\left( n^{\frac{1}{\lceil d/2 \rceil}} \right)^{\lceil d/2 \rceil} \log p = O(n \cdot \lambda) \\ &( \text{ by } \ell = n^{\lceil d/2 \rceil}, \log p = \lambda) \\ \left| S_2^{(k)} \right| &= 2B \cdot t \cdot \sqrt{c} \cdot \log p \\ &= \frac{2mt}{\ell^{\delta}} \cdot t \cdot t\ell^{\delta/2} \cdot \log p = \frac{2mt^3 \log p}{\ell^{\delta/2}} \\ &( \text{ by } B = \frac{mt}{\ell^{\delta}}, \ c = t^2 \ell^{\delta} ) \\ &= \frac{2m\lambda^4}{\ell^{\delta/2}} \\ &( \text{ by } t = \log p = \lambda) \end{split}$$

Because  $\ell^{\delta/2} = n^{\frac{\delta}{2\lceil\frac{d}{2}\rceil}}$ ,  $m = n^{\tau}$ , and  $k \in [\lambda]$ , we have:  $|\mathrm{sd}| = 1 + \sum_{k \in [\lambda]} \left( \left| \boldsymbol{b}^{(k)} \right| + 2 \left| S_1^{(k)} \right| + 2 \left| S_2^{(k)} \right| + 1 \right)$  $= O((n + n^{\tau - \frac{\delta}{2\lceil\frac{d}{2}\rceil}}) \cdot \lambda^5)$ 

We set  $n' = O(n + n^{\tau - \frac{\delta}{2\lceil \frac{d}{2} \rceil}})$ , therefore  $m = n'^{\tau'}$  for some  $\tau' > 1$  and the seed length is  $n' \text{poly}(\lambda)$ . This concludes the proof.

Proof of Pseudorandomness. We prove the following proposition which implies that sPRG is pseudorandom and the security loss from the Boolean PRG and the LPN<sub>F</sub> assumption is polynomial in  $\lambda$ . Therefore, our sPRG is polynomially or subexponentially secure, if the underlying Boolean PRG and the LPN<sub>F</sub> assumptions are polynomially or subexponentially secure respectively.

PROPOSITION 4.2. For parameters as set in the Construction, if  $\delta$ -LPN<sub>F</sub> is  $\epsilon$ -hard, and PRG is  $\epsilon$ -pseudorandom, the following two distributions are  $\gamma$ -indistinguishable, where  $\gamma = O(\lambda \epsilon)$ .

$$\mathcal{H}_0 = \left\{ (I, \phi, \mathbf{A}, \{ \boldsymbol{b}^{(k)} \}_{k \in [\lambda]}, \operatorname{err}, \boldsymbol{z}) \right\}$$

$$\mathcal{H}_3 = \left\{ (I, \phi, \mathbf{A}, \{ \boldsymbol{b}^{(k)} \}_{k \in [\lambda]}, \operatorname{err}, \boldsymbol{r}) \right\},$$

where  $I' \leftarrow \text{IdSamp}'(1^{\lambda}, 1^{n'}), (P', S') \leftarrow \text{SdSamp}'(I'),$   $z \leftarrow \text{Eval}'(I', \text{sd}), \text{ and } r \leftarrow \{0, 1\}^m. (Recall that I' = (I, \phi, A),$  $P' = (\{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, \text{err}).)$  **PROOF.** We show that  $\mathcal{H}_0$  and  $\mathcal{H}_3$  are  $\gamma$ -indistinguishable via the following two hybrids.

$$\mathcal{H}_{1} = \left\{ (I, \boldsymbol{\phi}, \mathbf{A}, \{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, [0, \boldsymbol{y}]) \right\}$$
$$\mathcal{H}_{2} = \left\{ (I, \boldsymbol{\phi}, \mathbf{A}, \{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, [0, \boldsymbol{r}]) \right\}$$

where  $I' \leftarrow \mathsf{IdSamp}'(1^{\lambda}, 1^{n'}), (P', S') \leftarrow \mathsf{SdSamp}'(I'),$ 

 $\mathbf{y} \leftarrow \text{Eval}(I, \boldsymbol{\sigma})$ , and  $\mathbf{r} \leftarrow \{0, 1\}^m$ . Observe that the only difference between  $\mathcal{H}_0$  and  $\mathcal{H}_1$  (highlighted in the box) is that in the former err is the flag set by the SdSamp' algorithm and the sPRG output  $\mathbf{z} = (1 - \text{err})\mathbf{y}$ , whereas in the latter err is replaced with a constant 0 and the output is replaced with  $\mathbf{y}$ . Similarly, the only difference between  $\mathcal{H}_2$  and  $\mathcal{H}_3$  (highlighted in the box) is whether err is replaced with 0. We show in Claim 4.2 that the probability that err = 1 is exponentially small.

CLAIM 4.2. For parameters as set in the Construction, it holds that  $\Pr[\text{ err } = 1] \leq 2^{-\lambda}$ , where  $\operatorname{IdSamp}'(1^{\lambda}, 1^{n'})$ , and  $(P' = (\{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, \operatorname{err}), S') \leftarrow \operatorname{SdSamp}'(I')$ .

Therefore, the statistical distance between  $\mathcal{H}_0$  and  $\mathcal{H}_1$ , as well as  $\mathcal{H}_2$  and  $\mathcal{H}_3$ , is bounded by  $2^{-\lambda}$ . Due to space limit, we defer the proof to the full version [91].

It remains to show that  $\mathcal{H}_1$  and  $\mathcal{H}_2$  are  $O(\lambda\epsilon)$ -indistinguishable. This follows from the facts that i)  $\phi$  is completely independent of  $(I, \mathbf{A}, \{\mathbf{b}^{(k)}\}_{k \in [\lambda]}, \mathbf{y})$  or  $(I, \mathbf{A}, \{\mathbf{b}^{(k)}\}_{k \in [\lambda]}, \mathbf{r})$ , and ii)  $(I, \mathbf{A}, \{\mathbf{b}^{(k)}\}_{k \in [\lambda]}, \mathbf{r})$ , and ii  $(I, \mathbf{A}, \{\mathbf{b}^{(k)}\}_{k \in [\lambda]}, \mathbf{r})$ .

 $\{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, \boldsymbol{y}\}$  and  $(I, \mathbf{A}, \{\boldsymbol{b}^{(k)}\}_{k \in [\lambda]}, \boldsymbol{r})$  are indistinguishable following from the LPN assumption and the pseudorandomness of PRG. This indistinguishability is is the heart of the security of sPRG, and is captured in Lemma 4.3 below.

LEMMA 4.3. For parameters set as in the construction, assuming that (IdSamp, Eval) is a  $\epsilon$ -secure Boolean pseudorandom generator and  $\delta$ -LPN<sub>F</sub> is  $\epsilon$ -hard, the following two distributions are  $O(\lambda \epsilon)$ indistinguishable:

$$\mathcal{D}_0 = \left\{ I, A, \{ \boldsymbol{b}^{(k)} = \boldsymbol{s}^{(k)} \cdot A + \boldsymbol{e}^{(k)} + \sigma \}_{k \in [\lambda]}, \boldsymbol{y} \right\}$$
$$\mathcal{D}_{\lambda+1} = \left\{ I, A, \{ \boldsymbol{u}^{(k)} \}_{k \in [\lambda]}, \boldsymbol{w} \right\}$$

where  $A \leftarrow \mathbb{Z}_p^{\ell \times n}, s^{(k)} \leftarrow \mathbb{Z}_p^{1 \times \ell}, e^{(k)} \leftarrow \mathcal{D}_r^{1 \times n}(p), \sigma \leftarrow \{0, 1\}^{1 \times n}, I \leftarrow \mathsf{IdSamp}(1^{\lambda}, 1^n), y \leftarrow \mathsf{Eval}(I, \sigma), u \leftarrow \mathbb{Z}_p^{1 \times n}, and w \leftarrow \{0, 1\}^{1 \times m}.$ 

PROOF. We introduce intermediate distributions  $\mathcal{D}_i$  defined as follows:

$$\mathcal{D}_{i} = \left\{ I, \mathbf{A}, \{ \mathbf{u}^{(k)} \}_{k \in [i]}, \{ \mathbf{b}^{(k)} = \mathbf{s} \cdot \mathbf{A} + \mathbf{e}^{(k)} + \sigma \}_{i < k \le \lambda}, \mathbf{y} \right\}$$

where the first *i* LPN samples  $\boldsymbol{b}^{(1)} \cdots \boldsymbol{b}^{(i)}$  are replaced with random strings  $\boldsymbol{u}^{(1)} \cdots \boldsymbol{u}^{(i)}$ .

Now observe that for every  $i \in [\lambda]$ ,  $\mathcal{D}_{i-1}$  and  $\mathcal{D}_i$  are  $\epsilon$ -indisting ushable following immediately from the  $\epsilon$ -indistinguishability of the  $\delta$ -LPN<sub>F</sub> assumption. Finally, observe that  $\mathcal{D}_{\lambda}$  and  $\mathcal{D}_{\lambda+1}$  are  $\epsilon$ -indistinguishable due to the  $\epsilon$ -security of the PRG (IdSamp, Eval). Therefore, the lemma holds.

# 5 BOOTSTRAPPING TO INDISTINGUISHABILITY OBFUSCATION

A sequence of prior works led to the work of [68] that constructs *iO* from a structured-seed PRG, and the bilateral-DLIN assumption and LWE assumptions. We first recall their theorem (See Theorem 10.2 in [68]):

THEOREM 5.1 ([68]). Let  $\tau$  be arbitrary constant greater than  $0, \epsilon$ in (0, 1) and  $d \in \mathbb{N}$  be any constants. Assume sub-exponential security of the following assumptions, where  $\lambda$  is the security parameter, p is a  $\lambda$ -bit prime, and the parameter k is a large enough polynomial in  $\lambda$ :

- Bilateral-DLIN assumption on asymmetric bilinear groups of order p (implied by the DLIN assumption on symmetric bilinear groups of order p),
- LWE assumption over Z<sub>p</sub> with subexponential modulus-tonoise ratio 2<sup>k<sup>€</sup></sup>, where k is the dimension of the LWE secret.
- Existence of a structured-seed PRG with stretch  $1 + \tau$  and complexity (deg d, deg 2) over  $\mathbb{Z}_p$  as in Definition 3.1.

Then, there exists a (subexponentially secure) indistinguishability obfuscation scheme for all circuits. Further if these assumptions are polynomially secure, then there exists a compact functional encryption scheme for all circuits.

Combining the Theorem above with Theorem 4.1, where we construct a structured seed PRG, we get the the following theorem.

THEOREM 5.2. Let  $\tau$ ,  $\epsilon$  be arbitrary constants greater than 0, and  $\delta$  in (0, 1) be a constant. Assume sub-exponential security of the following assumptions,

- the LWE assumption with subexponential modulus-to-noise ratio  $2^{k^{\epsilon}}$  and noises of magnitude polynomial in k, where k is the dimension of the LWE secret,
- the  $\delta$ -LPN<sub> $\mathbb{F}$ </sub> assumption (as in Definition 2.3),
- the existence of a Boolean PRG in NC<sup>0</sup> with stretch  $1 + \tau$ ,
- Bilateral-DLIN assumption on asymmetric bilinear groups of prime order (implied by the DLIN assumption on symmetric bilinear groups of prime order),

Then, (subexponentially secure) indistinguishability obfuscation for all polynomial-size circuits exists. Further if these assumptions are polynomially secure, then there exists a compact functional encryption scheme for all circuits.

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