# **Performance Portable Back-projection Algorithms on CPUs: Agnostic Data Locality and Vectorization Optimizations**

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## ABSTRACT

Computed Tomography (CT) is a key 3D imaging technology that fundamentally relies on the compute-intense back-projection operation to generate 3D volumes. GPUs are typically used for backprojection in production CT devices. However, with the rise of power-constrained micro-CT devices, and also the emergence of CPUs comparable in performance to GPUs, back-projection for CPUs could become favorable. Unlike GPUs, extracting parallelism for back-projection algorithms on CPUs is complex given that parallelism and locality are not explicitly defined and controlled by the programmer, as is the case when using CUDA for instance. We propose a collection of novel back-projection algorithms that reduce the arithmetic computation, robustly enable vectorization, enforce a regular memory access pattern, and maximize the data locality. We also implement the novel algorithms as efficient back-projection kernels that are performance portable over a wide range of CPUs. Performance evaluation using a variety of CPUs from different vendors and generations demonstrates that our back-projection implementation achieves on average 5.2× speedup over the multithreaded implementation of the most widely used, and optimized, open library. With a state-of-the-art CPU, we reach performance that rivals top-performing GPUs.

ICS '21, June 14-17, 2021, Virtual Event, USA

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ACM ISBN 978-1-4503-8335-6/21/06...\$15.00

https://doi.org/10.1145/3447818.3460353

#### **CCS CONCEPTS**

 $\bullet Computing methodologies \rightarrow Vector / streaming algorithms.$ 

#### **KEYWORDS**

Computed Tomography, Data Locality, Vectorization

#### **ACM Reference Format:**

Peng Chen, Mohamed Wahib, Xiao Wang, Shinichiro Takizawa, Takahiro Hirofuchi, Hirotaka Ogawa, and Satoshi Matsuoka. 2021. Performance Portable Back-projection Algorithms on CPUs: Agnostic Data Locality and Vectorization Optimizations. In 2021 International Conference on Supercomputing (ICS '21), June 14-17, 2021, Virtual Event, USA. ACM, New York, NY, USA, 13 pages. https://doi.org/10.1145/3447818.3460353

#### 1 INTRODUCTION

Computed Tomography (CT) is a key 3D imaging technology used in several fields such as medical analysis, scientific inspection, and non-intrusive testing. Back-projection is a fundamental kernel in most of the image reconstruction algorithms such as FDK [12] and iterative reconstruction algorithms [3, 4, 15] that employ the algorithms of forward-projection and back-projection, e.g. MLEM [21, 48], EM [16], ART [45], and SART [1]. As a consequence, the backprojection kernel is effectively empowering 100,000s of production CT devices worldwide [30]. Due to its high computational cost  $(O(N^4)$  [28, 46]), back-projection is often the compute bottleneck for reconstructing 3D images. That is specially the case in iterative reconstruction algorithms at which back-projection is called repeatedly [61]. To meet the critical demands for rapid image reconstruction, in the past decades, a plethora of accelerators were employed to improve the computational performance of backprojection such as linear accelerators [53], ASICs [62], DSPs [27], FPGAs [8, 25, 64], and distributed systems [6, 13, 19, 38, 57, 65]. Custom processors have been adopted by production CT devices in

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the earlier generations of CT devices. However, over the last decade, GPUs have increasingly become the main processor used for image reconstruction, due to their programmability and competitive performance [4, 5, 10, 11, 31, 38, 41, 43, 63, 68, 69].

We argue that there are strong reasons for optimizing backprojection kernels for CPUs, despite GPUs being the de facto processor for back-projection over the last decade. First, there is a noticeable trend of portable and lightweight micro-CT devices [2, 7, 52, 66]. This further makes CT vendors more sensitive to the cost, power, and space requirements that come along with discrete accelerators. For instance, a state-of-art lineup of industrial micro-CT devices (listed in [34]) reports power consumption ranging between 225W to 450W. However, low-end GPUs would consume up to 30% of that power budget, and high-end GPUs (i.e. Nvidia's P100 [35] and V100 [36]) would consume almost 100% of the power budget of a micro-CT device [36]. Second, high-performance CPUs with high bandwidth memory can compete with GPUs in raw performance and performance to power. For example, Fujitsu's A64FX ARM processor [67] is equipped with HMB2 memory [22] and SVE (Scalable Vector Extension) [50], which pushes the performance to be comparable to the top of line GPUs in a variety of workloads [44]. Third, integrating accelerators into CT systems increases the system complexity and leads to considerable costs, i.e., hardware and software development. The use of performant CPUs would be preferable since they are a basic component in most CT devices. To conclude, it is crucial to revisit back-projection for CPU, in light of those developments.

We propose novel optimizations at the algorithm-level, and not at the target CPU hardware level. We argue that those carefully designed and novel optimizations at the algorithm level are key to performance portability on a variety of CPUs, having different characteristics. This is in contrast to previous work on CPU backprojection that engineers target-specific optimizations to a single dedicated CPU target [20, 24, 26, 54, 55]. It is important to note that, in the context of performance portability, CPUs have much more divergence in characteristics to optimize for, in comparison with GPUs. For instance, the vector width changes from generation to generation (for even the same vendor), while in Nvidia GPUs, the unit of execution (CUDA warp [9]) maintained the same size in all generations.

We briefly list here the algorithm-level optimizations for backprojection we propose for CPUs. First, effective data locality and regular memory access patterns are critical to improving the efficiency of back-projection kernels. To improve the data locality [49], we introduce a generic scheme to reschedule the loop ordering, employ data blocking, and change the data layout rearrangement. This is in contrast to other non-portable methods that rely on the gather load intrinsic to deal with the irregular memory access [54]. Second, assuring that the SIMD vector units of the CPUs are fully utilized is crucial for performance. Designing back-projection algorithms that are fully vectorizable by compilers, yet being performance portable across CPUs from different vendors is necessary, yet challenging. To assure a loop could vectorize automatically, it is necessary to simplify the iteration space and data accesses such that it can be auto-vectorized by the compiler. We hence gear our optimization techniques to enable the compiler to consistently vectorize the

back-projection kernel, regardless of the vector width. Third, backprojection requires high computational resources. It is crucial to improve its performance at the algorithmic level by reducing the arithmetic computation and memory access to the least possible. To that end, we propose a novel double-buffer sub-line algorithm for efficient interpolation at the sub-pixel precision. We also reduce the number of arithmetic operations, by exploiting geometric symmetry, while maintaining contiguous memory access. In addition, we block the sub-line interpolation, to improve the locality without any impediment to the compiler's capability of automated vectorization.

The contributions in this paper are:

- We propose a collection of novel algorithm-level optimizations for back-projection on CPUs. Our optimizations reduce the computational cost of the projection operations, improve data locality, and consistently enable auto-vectorization.
- We provide a performance portable implementation: a single OpenCL source for all CPUs supporting OpenCL and a single OpenMP source for CPUs that do not support OpenCL (without any trace of processor-specific intrinsics or optimizations).
- We demonstrate that our kernels achieve on average 5.2× speedup over the most widely used and optimized multi-threaded library, on a variety of CPUs from different vendors and generations. Also, we achieve better performance on the Fujitsu A64FX than commonly used open-source CUDA implementation of back-projection on Nvidia V100 Volta GPU (when accounting for unavoidable data movement overhead between host and device).

The rest of this paper is organized as follows: In Section 2, we introduce the background. Section 3 illustrates the proposed algorithms. Section 4 shows the evaluated result. In Section 5, we elaborate on the related work. Finally, Section 6 concludes.

## 2 BACKGROUND

In this section, we introduce the details of image reconstruction using the back-projection algorithm and describe the basics of OpenCL for multicore CPUs.

## 2.1 CT Image reconstruction

This section illustrates the 3D image reconstruction algorithm for Cone-Beam Computed Tomography (CBCT) as presented by Feldkamp et al. [12], including the CBCT geometry and back-projection algorithm.

2.1.1 **Geometry of CT system.** Figure 1 shows the triangular geometry of CBCT system [45]. The X-ray source is some form of a microfocus X-ray tube. The Flat Panel Detector (FPD) is a digital radiography imaging sensor, similar to that of digital photography. The distances of the source to the rotation axis (the Z-axis) and FPD are *d* and *D*, respectively. The width and height dimensions of the FPD, in the unit of pixel, are **nw** and **nh**, respectively. Note that the V-axis of FPD is parallel to Z-axis. The sizes of 3D volume data in a unit of voxel are **nx**, **ny**, and **nz**. The default data layout of volume data is row-major order. As a typical pinhole model [17], all geometric information can be presented as a matrix of size 3×4 (called *projection matrix*), which is used for back-projection computation,

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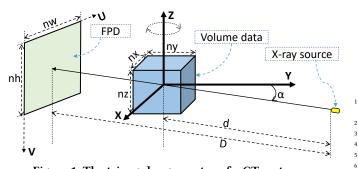


Figure 1: The triangular geometry of a CT system.

Listing 1: Pseudocode of the multi-threaded back-projection 11 implementation adopted by RTK and RabbitCT. The np is the 12 number of projections. The sizes of the 2D projections, 3D volume data, and projection matrices are nh×nw, nz×nx×ny, and 3×4, respectively.

```
void BackProjection(float img[np][nh][nw], float mat[np
  ][3][4], float volume[nz][ny][nx])
{
   for
       (int \ s = 0; \ s < np; \ s++)
    parallel_for (int k = 0; k < nz; k++)
      for (int j = 0; j < ny; j++){
        for (int i = 0; i < nx; i++) {
             float vec[4] = {i, j, k, 1. f}; // coordinate
             float z = dot4(mat[s][2], vec); // dot
            float f = 1.f/z;
            float x = dot4(mat[s][0], vec)*f; //dot
float y = dot4(mat[s][1], vec)*f; //dot
             float val = Bilinear_Interpolate(img[s], x, y);
            float w = f * f; // compute weight
            volume[k][j][i] += val*w; // update
   } } } }
            // s, k, j, i
```

e.g. projecting a voxel to the plane of FPD as mat in Listing 1. For simplification, we ignore the computation of obtaining the projection matrix via the geometry information. The detailed formulation is available at [58]. Computing the projection, i.e., mapping a 3D point (i, j, k) to the plane of FPD (the UV plane) is shown in Listing 1 (lines 7~11).

2.1.2 Back-projection. The computational complexity of backprojection is  $O(N^4)$  [28, 46]. Listing 1 shows the pseudocode of the reference multi-threaded back-projection algorithm as implemented in the widely used RTK library<sup>1</sup>. Note that the pseudocode also matches the multi-threaded implementation of the other most widely used library, RabbitCT [42]. A parallel loop (line 4) is where the work is divided among the threads to take advantage of the multi-cores of the processor. The inputs to the back projection kernel are: the 2D filtered projections img, the 3D output volume of the filtering computation volume, and the dimensions expressed as (np, nh, nw). Throughout this paper, np refers to the total number of projections, and **nb** refers to the number of projections in a single batch. More specifically, the projections are equally split into disjoint subsets of projections, where each subset includes a

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Listing 2: The Bilinear\_Interpolate is a bilinear interpolation function. The dot4 is an inner product function and the mix is a built-in common function (e.g. in OpenCL) that returns a linear blend of two variables, where Type can be scalar or vector data types.

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float Bilinear_Interpolate(float img[nh][nw], float x, float
  y){
  int nx = (int)x; // convert to integer
  int ny = (int)y; // convert to integer
  //horizontal interpolation using mix
  float s0 = mix(img[ny], img[ny])
                                         ][nx+1], x-nx);
  float s1 = mix(img[ny+1][nx], img[ny+1][nx+1], x-nx);
  //vertical interpolation using mix
  return mix(s0, s1, y-ny);
float dot4(float v0[4], float v1[4]){//inner product
    return v0[0]*v1[0]+v0[1]*v1[1]+v0[2]*v1[2]+v0[3];
Type mix(Type x, Type y, Type a){//linear interpolation
    return x * ((Type) 1.f-a) + y * a;
}
```

number of projections *nb* that is used to update the volume data as a batch.

The projection matrix **mat** is used to project the voxel to the FPD plane. The projected coordinate is written as x and y in lines 10 and 11. The value of *z* (line 8) is used to derive the projection coordinate, as well as a weighting factor to update the volume data in lines 13~14. The **dot4** operator (line 10), is a customized operator for the inner product as in Listing 2 that is called three times in the most inner loop. As listing 2 shows, a bilinear interpolation function, namely Bilinear\_Interpolate, is used to fetch the intensity value of the 2D projections.

## 2.2 Parallel Computation by OpenCL

OpenCL provides a portable way to take advantage of the SIMDaccelerated processors with a programming approach of single instruction multiple threads. The OpenCL kernels can be compiled dynamically at runtime for the target architecture. OpenCL abstracts an open standard platform for parallel programming and provides a uniform programming interface that is often used to write portable and efficient code for a variety of processors, e.g. CPUs, GPUs, and FPGAs. As an open standard defined by Khronos Group [59], OpenCL uses a host/device programming model. Programming by OpenCL involves writing host code, often in C/C++, that runs on the host, and a kernel code developed in C, that runs on the device or accelerator. The host codes are often compiled by a general C/C++ compiler (e.g. gcc/g++), and the kernel codes are built at runtime by a target-specific compiler. The computing unit in OpenCL is a work-item. A work-group is a collection of work-items abstracted as a multi-dimensional descriptor named NDRange. Note that the work-items within the same work-group can exchange data using the local memory.

## 2.3 Terminology

We define the image reconstruction problem and performance metrics in this section. The image reconstruction problem is defined as  $nw \times nh \times np \Rightarrow nx \times ny \times nz$ , where  $nw \times nh$  and np are the size and

<sup>&</sup>lt;sup>1</sup>Reconstruction Toolkit (RTK): https://www.openrtk.org

number of input projections, respectively. The size of the output volume is defined as  $nx \times ny \times nz$ . The performance metric we use is calculated as  $nx \times ny \times nz \times np \times t^{-1} \times 10^{-9}$ , where *t* denotes the run-time in a unit of second. The performance unit of the kernel commonly used in image reconstruction algorithms is **GUPS**, which stands for Giga Updates per Second.

## 3 PROPOSED BACK-PROJECTION ALGORITHMS

In this section, we discuss a variety of algorithms for back-projection that employ different optimization schemes corresponding to different CPU features, without loss of generality with regard to CPU vendor and generation. The optimization schemes do not interfere with each other and could be combined. We improve the data locality and memory access pattern by transposing projections and volume data. To fully utilize the multi-cores and vector units, we also take advantage of both OpenMP and OpenCL to speed up the back-projection computation. Auto-vectorization can be performed at compile time when using either OpenMP or OpenCL. Without explicit vector instructions, and by carefully writing the code, the OpenCL compiler [37] can automatically vectorize instructions by packing multiple work-items together and running them in a SIMD fashion. To improve the data locality and reduce the arithmetic computation, we use a novel sub-line algorithm to cache the interpolated data at sub-pixel precision. More specifically, we improve the data locality of the bilinear interpolation while enabling auto-vectorization.

## 3.1 Algorithmic Optimizations

This section describes the algorithmic changes we introduce to improve the memory access pattern, reduce arithmetic computation, and expose data-parallel operations. These optimizations apply to both single-core and multicore. Those optimizations at the algorithmic level are key to performance portability.

As shown in Listing 1, the back-projection is driven by voxels, which are updated by their mapped pixels at projections. The values of voxels are independently updated, i.e. an embarrassingly parallel point-wise operation. Hence, the algorithmic optimizations introduced should, in theory, scale with the capacity of computing resources, i.e. number of cores. The following section elaborates on the algorithmic optimizations.

3.1.1 **Towards a Regular Memory Access Pattern.** As Listing 1 shows, each voxel is updated by the intensity of projections, according to the projected position at sub-pixel precision using the bilinear interpolation scheme in Listing 2. Figure 2 can be used to visualize the mechanism of bilinear interpolation and also illustrates the optimization of back-projection by transposing the projection and volume data. As shown in the figure, the memory access for the bilinear interpolation along the line  $\bar{a}$  is irregular (sloped access to a 2D mesh). Furthermore, the slope of line  $\bar{a}$  changes with the rotation angle of  $\alpha$ . However, the projections of blue voxels (parallel to Z-axis) are in a line  $\bar{b}$ , which is invariably parallel to V-axis and Z-axis. Therefore, the memory access along line  $\bar{b}$  would be regular and unit-strided, if we transpose the projections.

The proposed Algorithm 1 shows the optimization using transposed projections and volume data. More specifically, we optimize

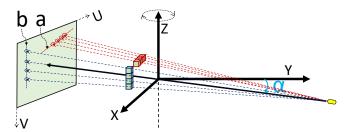


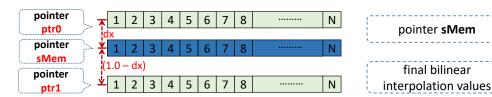
Figure 2: The motivation for transposing projections and volume data. The red and blue voxels are parallel to the X-axis and Z-axis, such that their projection points on the UV plane are lines  $\bar{a}$  and  $\bar{b}$ , respectively. The slope of line  $\bar{a}$  changes according to the rotation angle  $\alpha$ . The line  $\bar{b}$  is invariably vertical (namely parallel to both V-axis and Z-axis).

**Algorithm 1:** Proposed back-projection. *nb* is the batch number. The transposed projection and volume data are presented as *img* and *volume*, respectively.

	Input	Input :img[nb][nw][nh], mat[nb][3][4], volume[nx][ny][nz],					
		vecIJ					
		:volume[nx][ny][nz]					
		a omp parallel for	▹ launch multiple threads				
		$\{i, j\} \in vecI \mathcal{J} \mathbf{do}$					
3		$\leftarrow \{i, j, 0, 1.f\}$	▶ k is independent of X				
4		s = 0 to $nb - 1$ do	6.6				
5		$F[s] \leftarrow 1.f/dot4(mat[s][2], vec)$	⊳ an array of f				
6	1 1	$W[s] \leftarrow F[s]^*F[s]$	⊳ an array of w				
7	L	$X[s] \leftarrow dot4(mat[s][0], vec)^*F[s]$	an array of x				
8	for	$s = 0 \ to \ nb - 1 \ \mathbf{do}$					
9		nx←(int)X[s]	▶ type convension				
10		#pragma omp simd					
11		for $m=0$ to $nh-1$ do					
12			[nh][m], img[s][nh+1][m],				
		$\sum X[s]-nX)$	⊳ see Figure 3				
13	for	$k = 0 \ to \ \frac{nz}{2} - 1 \ do$					
14		sum←0	▹ register for partial results				
15		_sum←0	▹ register for partial results				
16		vec[2]←k;	⊳ update index k				
17		#pragma omp simd					
18		for $s = 0$ to $nb - 1$ do					
19		pMem←sMem[s]					
20		y←dot4(mat[s][1], vec)*F	[s] ► projection comp.				
21		ny←((int)y	▶ type conversion				
22		sum+=mix(pMem[ny], pM	1em[ny+1], y-ny)*W[s]				
23		y←nh - 1 - y	▷ geometry symmetry				
24		ny←(int)y	▶ type conversion				
25		_sum += mix(pMem[ny],	pMem[ny+1], y-ny)*W[s]				
26		volume[i][j][k] += sum	⊳ update volume data				
27		volume[i][j][nz-1-k] += _sum	-				
			±				

the data access pattern by transposing the two-dimensional projections *img* (input argument). Note that the transposing operation requires marginal time in comparison to the back-projection and can be performed immediately after the pre-processing filtering

N elements



(a) *ptr0* and *ptr1* are pointers to two neighbouring vertical rows. *sMem* is a small-sized memory buffer for caching the sub-line values, where  $0 \le dx < 1$ .

Figure 3: Novel interpolation scheme using sub-line blending. (a) and (b) show vertical and horizontal interpolations.

1	#pragma omp simd	1	<pre>for (int i=0; i<n ++)="" 8;="" i="" pre="" {<=""></n></pre>	
2	<pre>for (int i=0; i<n; i++)<="" pre=""></n;></pre>	2	float8 v0 = <b>vload8</b> (i, ptr0);	//load float8
3	{	3	float8 v1 = <b>vload8</b> (i, ptr1);	//load float8
4	//blend two lines of pixels	4	float8 v2 = $mix(v0, v1, dx);$	//blend float8
5	sMem[i] = <b>mix</b> (ptr0[i], ptr1[i], dx)	5	<pre>vstore8(v2, i, sMem);</pre>	//store float8
6	}	6	}	

#### (a) Vectorize sub-line alg. by C (OpenMP).

(b) Vectorize sub-line alg. by OpenCL.

(b) The bilinear interpolation in the horizontal direction. Each output value rely

on two neighboring values at the sub-line buffer.

#### Figure 4: Implementations of proposed bilinear interpolation scheme. mix is a linear interpolation function in Listing 2.

step, and before the back-projection starts. We also reorganize the loop that iterates the voxels along the Z-axis in the original volume to get unit-strided access over the transposed volume data as the input argument *volume* shows. More importantly, this transposition operation builds a foundation for further optimizations such as exploiting symmetry and improving the bilinear interpolation scheme to be vectorizable.

3.1.2 Reducing Arithmetic Computation. We reduce the volume of the arithmetic computation by: a) capitalizing on the loop reordering introduced with transposition by moving the loop invariant compute portions outside the inner loop (i.e. hoisting), and b) exploiting geometric symmetry. The data reuse is shown in lines  $4 \sim 7$  of Algorithm 1. The intermediate variables (namely F, W, and *X*) are only computed once and their results are shared by the inner loops for further computations, e.g. projection operation, weighting projection intensity, and updating the volume data. Note that the variables F, W, and X contain a small set of values of f, w, and x, respectively (as shown in Listing 1). In this optimization, we exploit the geometric characteristics of back-projection where the values of *z* and *x* are constant values when *i*, *j*, and the projection matrix (with projection angle  $\theta$ ) are fixed values. That is since the value of z is equal to  $d - x\cos\theta - y\sin\theta$  (as introduced in [23, 28]), where x and y are coordinates of voxels that correspond to the indexes of *i* and *j*. The values of *x* are also independent of *i* and *j*. That is since the projections of voxels are parallel to the V-axis (or Z-axis in Figure 1) at the FPD plane, as introduced in Section 2.1.1.

We also reduce the arithmetic computation by exploiting the geometric symmetry proposed by Zhao et al. [68]. As Figure 1 shows, for two voxels that are symmetric to the XY plane, their projections at FPD (namely UV plane) are symmetric to the horizontal centerline of FPD. As line 13 of Algorithm 1 shows, we exploit this symmetry by only performing half of the projection computations for *y*; the symmetric positions of *y* can be derived using a single instruction rather than using complex dot and multiply operations. More specifically, the original number of dot operations is 3 \* np \* nx \* ny \* nz, the optimized number of operations is reduced to np\*nx\*ny\*(1+nz/2). Hence, the ratio of reduced computations for dot operations may be written as  $(5 - 2/nz)/6 \approx 5/6$  since  $nz \gg 2$  in real-world applications.

*3.1.3* **Improving locality & Reducing Memory Accesses.** We present the details of reducing memory accesses for improving back-projection performance in this section. As discussed by Treibig et al. [54] and Hofmann et al. [20], back-projection is a bandwidth-limited algorithm and the performance bottleneck is the voxel updates (see line 14 of Listing 1). In addition to the coalesced access to the memory of volume data proposed by Treibig et al. [54], we improve the algorithm by further reducing the memory accesses to volume data. In the following paragraphs we elaborate.

First, existing multi-threaded implementations (shown in Listing 1) do not split the projections into subsets. This is since the interpolation schemes used in those implementations would reduce the locality if accessing multiple images in the same batch. Contrarily, using batches of images to update the volume is preferable since that would reduce the writes to memory. To use batched projections, one would require a new interpolation scheme to address this issue (more on that in Section 3.1.5).

Second, we reduce memory access to the volume data by using registers to accumulate the partial results (as shown in Algorithm 1). In line 14~27, we use two registers to accumulate the partial results of voxels for multiple projections in batched processing, then update the volume data by the values accumulated in the registers. Since the cost of accessing registers is negligible with regard to memory access, this batched processing for projections and accumulation of partial results can significantly reduce the number of memory accesses. The original number of elements accessed for the volume data is 4 \* np \* nx \* ny \* nz, while after the optimization the number of elements accessed drops to 4 \* np \* nx \* ny \* nz/nb. Therefore, in comparison to the baseline implementation, the ratio of memory accesses to the volume data becomes 1/nb. With a larger value of

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Listing 3: A performance-portable OpenCL kernel for backprojection implementing our optimization techniques, e.g. transposing projection and volume data, reordering loops, reusing variables after hoisting, and exploiting geometric symmetry. Constant memory is used to store the projection matrix (see mat), global memory for the projections (see img) and volume data (see volume), where ROWS=3, imgDim=(np, nw, nh), volDim=(nx, ny, nz), LM\_SIZE=imgDim.x=nh.

```
kernel void bp_optimized( __constant float4* mat, __global float* img,
          int3 imgDim, global float* volume, int3 volDim, global int2* vecIJ)
         int k = get_global_id(0); //global index x
           local int2 ij; //share index i and j
           local float F[nb], X[nb], W[nb], sMem[LM_SIZE];//local mem
         if (k == 0) ij = vecIJ[get_global_id(1)]; //i and j \leftarrow global index y
         barrier(CLK_LOCAL_MEM_FENCE); //barrier for local memory
         float4 ijkw = (float4)(ij.x, ij.y, k, 1.f); //vector (i,j,k)
         if (k < nb) {
            float z = 1.f / dot((mat + ROWS * k)[2], ijkw);//compute z
            X[k] = dot((mat + ROWS * k)[0], ijkw)*z; //compute x
            F[k] = z; //cache z
            W[k] = z * z; //compute weight
         barrier(CLK_LOCAL_MEM_FENCE); //barrier for local memory
         float2 sum = (float2)(0.f. 0.f):
         for (int s = 0; s < nb; s++, img += imgDim, xâLUimgDim, y; mat += ROWS)
            int nx = convert int(X[s]);
            float dx = X[s] - convert float(nx):
            __global floate ptr0 = img + nx * imgDim.x; //see Fig. 3a
__global floate ptr1 = ptr0 + imgDim.x; //see Fig. 3a
for (int m = k; m < LM_SIZE; m += get_local_size(0))
                sMem[m] = mix(ptr0[m], ptr1[m], dx); //see Fig. 4
            barrier(CLK_LOCAL_MEM_FENCE); //barrier for local memory
            float y = dot(mat[1], ijkw)*F[s];
            float _y = width - 1 - y; //y and _y are symmetric at the vertical line of FPD
            int2 ny = convert_int2((float2)(y, _y));//float \rightarrow int
            float2 dy = (float2)(y, _y) - convert_float2(ny);
            //linear interpolation and update sum as in Fig. 3b
            sum += mix((float2)(sMem[ny.x], sMem[ny.y]), (float2)(sMem[ny.x+1],
               sMem[ny.y+1]), dy)*W[s];
            barrier(CLK LOCAL MEM FENCE); //barrier for local memory
         int offset = ij.y*volDim.z*volDim.x + ij.x * volDim.z;
37
38
         volume[offset + k] += sum.x; //update volume
         volume[offset + volDim.z - 1 - k] += _sum.y; //update volume
```

*nb*, we can move the bandwidth bottleneck of the memory access from volume data to projections. More specifically, the performance of the proposed algorithm may be bounded by the memory accesses to the projections for the bilinear interpolations. In Section 4, we demonstrate the performance gain of this optimization.

Finally, we also propose a novel bilinear interpolation with vectorization. This optimization can also reduce the memory accesses to some extent as will be discussed in later sections.

3.1.4 Vectorizating Operations. In this section, we present the details of how to enable full utilization of the vector units in the projection operation and interpolation computation in back-projection. Regarding the projection computation (e.g. the computation of *x*, *y*, and z in Listing 1), each inner product is performed on two vectors of size 1×4 at single precision. This inner product operation can be perfectly performed by the vector unit, e.g. the built-in function dot

is employed for this computation in our OpenCL-based implementation (as will be elaborated later). Note that we benefit from the well-aligned projection matrix of size 3×4 in back-projection algorithms. This is in contrast to other back-projection algorithms, such as in [28], at which the projection computation is not vectorizable due to the irregular memory access and compute pattern.

Since we use transposed projections and volume data, the linear interpolation operation can perform the memory accesses in a regular pattern and its computation can also be well vectorized with wide SIMD registers. In Algorithm 1, we can use vector registers for the accumulation of the variables sum and sum.

In addition, the computation of the second linear interpolation, as in Figure 3b, can be vectorized. More specifically, the memory accesses to update the volume data (lines 26 and 27 in Algorithm 1) can be performed in a vectorized fashion. This vectorized operation can improve the effective memory bandwidth for back-projection, especially in CPUs with High Bandwidth Memory (HBM), e.g. Fujitsu A64FX processor.

3.1.5 A Novel Bilinear Interpolation Scheme. This section discusses our scheme for bilinear interpolation. In linear interpolation, we calculate the target element as a weighted sum from two contributors. In this paper, we use the OpenCL built-in, vectorizable, mix linear blending function [32]. Bilinear interpolation performs a two-dimensional interpolation on a rectilinear grid and thus, it estimates an output element with four known contributors. The conventional back-projection algorithm can not perform the compute pattern in a regular access pattern: bilinear interpolation must be processed on a per-point basis since the data points of the projections are distributed on a sloped line. We capitalize on the transposition of the projections and volume to introduce a bilinear interpolation scheme that blends two lines of pixels in the vertical direction, followed by interpolating in the horizontal direction, where both have a regular access pattern and could be vectorized.

We elaborate on the effect of the proposed scheme on the vectorization of operations: both memory accesses and arithmetic computations. In Figure 3a, the variables ptr0 and ptr1 represent the addresses of two neighboring lines in a transposed projection. We load the data from the memory in a coalesced pattern and thus, the wide SIMD intrinsic can be employed as in the code example in Figure 4b. Figure 4a shows an alternative vectorized OpenMP implementation. It is important to note the regular access pattern for the memory access and arithmetic computations, i.e. loading data from the memory of ptr0/ptr1, storing the result to the memory of sMem, and the linear interpolation operations by the mix function. More specifically, we benefit from this scheme as follows: first, we linearly blend two lines of projections (as ptr0 and ptr1 in Figure 3) into a line of data where all elements in these two lines are only accessed once in a regular pattern. Second, either of the linear interpolations in the horizontal or vertical direction could be easily parallelized. Third, the second linear interpolation can be performed via cache-optimized access of the intermediate memory buffer between the vertical and horizontal steps (sMem in line 12 in Algorithm 1) with better locality and fewer linear interpolation operations.

CPU Name	Intel Xeon E5-2630 v4	Intel Xeon E5-2650 v3	Intel Xeon Gold 6140	Intel Core i7-9700K	AMD EPYC-7452	ThunderX2 (ARM) CN9975	Fujitsu (ARM) A64FX
Cores	10	10	18	8	32	28	48
Threads/core	2	2	1	1	1	4	1
Frequency	2.2GHz	2.3GHz	2.3GHz	3.6GHz	2.35GHz	1.8GHz	2.2GHz
Sockets	2	2	2	1	2	2	1
GFLOPS (SP)	704	736	2,649	461	2,406	806	6,800
Max bandwidth	137GB/s	137GB/s	250GB/s	42GB/s	205GB/s	159GB/s	1024GB/s
L1d/L1i cache	32K/32K	32K/32K	32K/32K	32K/32K	32K/32K	32K/32K	3M/3M
L2/L3 cache	256K/25M	256K/25M	1M/25M	256K/12M	512K/16M	256K/32M	32M/-
TDP	85W	105W	140W	95W	155W	150W	~160W

Table 1: Evaluation environment.

## 3.2 OpenCL-optimized Back-projection

This section discusses how to take advantage of OpenCL features to implement a performance-portable collection of back-projection kernels based on the optimization techniques we introduced in previous sections.

*3.2.1* **OpenCL kernel implementation.** In Listing 3, we present a performance-portable OpenCL implementation of the back-projection kernel that includes all the discussed optimization techniques in Algorithm 1).

We elaborate on optimizations techniques Listing 3 as follows: (I) we use constant memory [14] to cache the projection matrix mat. The size of each projection matrix is as small as 48B (sizeof(float)\*4\*3) (II) we pre-pack the indices of *i* and *j* in volume data to an array (argument vecl] to bp\_optimized kernel). Each work-group processes all voxels in a single vertical line and thus the indices of *i* and *j* can be shared by all work-items in a work-group. We use local memory to share the indices as the variable *ij* shows. (III) We use local memory to cache the read-only shared data F, X, and W (line 6). Note that the batch number (namely *nb*) ranges between 1~32 in our implementations. Hence, the required size of local memory is *sizeo* f(float) \* B \* 3, which is within the capacity of local memory. All of these values are only computed once as in lines 11~16 and reused by all work-items in a work-group. Finally, a barrier is required, line 17, to synchronize all work-items in a workgroup. (IV) Using the proposed bilinear scheme (as in Section 3.1.5), we perform the linear interpolation via fast local memory (lines 23~26). More importantly, we can vectorize the computation as Figure 4 shows. (V) By using the pixel values in local memory (namely sMem), the second linear interpolation can be performed as shown in Figure 3b (line 34). (VI) This kernel also exploits the geometric symmetry to reduce the computation by reusing values computed at the symmetric point, i.e. the value is computed once to be used by the point and its symmetric point (shown as the variables y and \_y). We use two-dimensional NDRange to launch this kernel, the local and global parameters may be written as (nz/2, 1) and (nz/2, 1)sizeIJ), respectively. Note that sizeIJ is the number of elements in the array vecIJ. We emphasize the intrinsic that requires the id of work-items in bold font, e.g. get\_global\_id, get\_local\_size ..., etc. Several built-in functions are used in our implementation such as dot, mix, and convert\_T. For more explanations on these built-in functions, the reader can refer to literature as in [33].

Algorithm 2: Prefetching by dual loc	cal memory buffers.
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Input :	▶ the input argument are same to Listing 3
-	1 0 0
Output :volume	▷ the output volume
1local sMem[2][LM_SIZE]	▹ declear dual scratchpad memories
<sup>2</sup> sMem[0] $\leftarrow img[0]$	▶ prefetching by sub-line alg. as in line 26
$s sum \leftarrow 0$	▶ declear registers for accumulation
4 <b>for</b> $i = 0$ to $nb - 1$ <b>do</b>	
5   if $i + 1 < B$ then	
$6 \qquad sMem[(i+1)\%2] \leftarrow i$	mg[i+1] > prefetching without barrier
7 sum $\leftarrow sMem[i\%2]$	▷ accumulate result via subline
8 barrier(CLK_LOCAL_M	EM_FENCE)  ► barrier work-items
9 volume $\leftarrow sum$	▶ update volume by batched processing

3.2.2 **Prefetching.** The use of OpenCL allows us to take advantage of the double buffers technique to overlap the loading operation and computation. As shown in Algorithm 2, we declare dual local memory buffers, which are emphasized in gray color. Hence, we can perform the load operation (line *6*) and computation (line *8*) using different buffers. However, the allocated memory is doubled, and thus, the available size of processing projections will be limited by the capacity of local memory.

### 4 EVALUATION

In this section, we introduce the evaluation environment, report the results of our experiments, and discuss the advantages and limitations of the proposed algorithms.

## 4.1 Experiments Setup

Table 1 shows the CPUs we use to evaluate our implementation. We use the same OpenCL implementation on all CPUs, without any customization or optimization to specific targets. For CPUs for which we did not have access to vendor support of OpenCL, we use the same OpenMP implementation, namely the ARM and AMD processors in Table 1.

For the CPUs that use OpenMP library, we employ GCC 9.1.0 (with OpenMP 4.5) for compiling all kernel versions, and "-O3 -fopenmp -lpthread -std=c++11 -march=native -ftree-vectorize - ftree-slp-vectorize" compilation options. For A64FX processor we use Fujitsu's compiler FCC 4.0.0 with the compiler options "-Kopenmp -Kfast -Ksimd=auto -Kassume=memory\_bandwidth -O3 -Nlibomp". On the CPUs that use OpenCL, i.e. Intel CPUs, we use OpenCL SDK-2019.5.345 and runtime 18.1.0 for developing and running

Table 2: Back-projection kernel names and optimizations.

API	Name	Transpose	Share	Symmetry	Subline	LocalMem	Prefetching
	transpose_mp	$\checkmark$					
OpenMP	share_mp	$\checkmark$					
	symmetry_mp	$\checkmark$	$\checkmark$	$\checkmark$			
	subline_mp	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$		
	transpose_cl	$\checkmark$					
	share_cl	$\checkmark$	$\checkmark$				
	share_lm_cl	$\checkmark$	$\checkmark$			$\checkmark$	
OpenCL	symmetry_cl	$\checkmark$	$\checkmark$	$\checkmark$			
	symmetry_lm_cl	$\checkmark$	$\checkmark$	$\checkmark$		$\checkmark$	
	symmetry_pf_cl	$\checkmark$	$\checkmark$	$\checkmark$		$\checkmark$	$\checkmark$

Table 3: Evaluated image reconstruction problem sizes.

	Ũ		-
label	Problem	label	Problem
P1	$256^2 \times 512 \Longrightarrow 256^3$	P7	$1024^2 \times 512 \Longrightarrow 256^3$
P2	$256^2 \times 512 \Longrightarrow 512^3$	P8	$1024^2 \times 512 \Longrightarrow 512^3$
P3	$256^2 \times 512 \Longrightarrow 1024^3$	P9	$1024^2 \times 512 \Longrightarrow 1024^3$
P4	$512^2 \times 512 \Rightarrow 256^3$		
P5	$512^2 \times 512 \Rightarrow 512^3$	P10	$1024^2 \times 512 \Longrightarrow 1300^3$
P6	$512^2 \times 512 \Rightarrow 1024^3$		$(1300^3 \text{ volume is } \sim 8.2 \text{GB})$

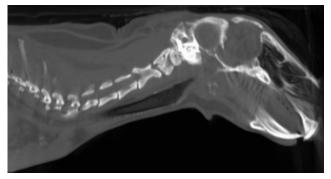


Figure 5: Using our algorithm to reconstruct the volume of the RabbitCT dataset [42] ( $512 \times 512 \times 512 \Rightarrow 512^3$ ). Rendered by ImageJ 3D Viewer [40].

OpenCL kernels. All OpenCL kernels are compiled with the compiler options "-cl-mad-enable -cl-fast-relaxed-math". Nvidia Tesla P100/V100 GPUs and CUDA 10.0 SDK [9] are also used for performance comparison.

In Table 2, we list a collection of back-projection kernels that apply the optimizations we proposed in Section 3. We take advantage of both OpenMP and OpenCL to optimize back-projection. The kernel variants of the employed optimization techniques are named "Transpose", "Share", ..., and "Prefetching" and they correspond to the optimization techniques discussed in Section 3.1. A collective combination of all optimization techniques (namely *symmetry\_pf\_cl* in Table 2) can be found in both Algorithm 1 and Listing 3 for the OpenCL version.

All evaluations are conducted at single precision. In Table 3, we list the image reconstruction problem sizes, which are labeled as P1,

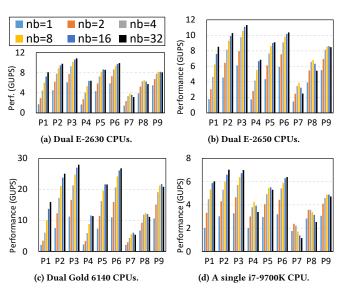


Figure 6: Performance of top performing kernel (symmetry\_pf\_cl) with different batch numbers (nb).

P2,..., P10. The sizes of the experimental projections include 256<sup>2</sup>,  $512^2$ , and  $1024^2$ ; the number of projections is fixed as 512; the output problems include  $256^3$ ,  $512^3$ ,  $1024^3$ , and  $1300^3$ . It is noteworthy that the required memory capacity to store a volume of size  $1300^3$  is ~8.2 GB.

## 4.2 Image Reconstruction Results

In Figure 5, we show an example of the generated volume data by our algorithms. The projections are from a real-world CT scanner described in the RabbitCT dataset [42]. Since the arithmetic computation is independent of the content of projections and volume data, we also use the volume data of RabbitCT to generate a variety of projections as described in Table 3 by a forward-projection tool in the RTK. To verify the output, we compare the reconstructed volume data with the results by RTK library, the Root Mean Square Error [60] threshold is less than 10e-5. Additionally, we employ the ImageJ [40] (an image processing tool) to render each generated volume data and manually inspect them.

## 4.3 Impact of Reducing Memory Accesses

This section discusses the performance impact of reducing memory accesses by batched processing. There are four kernels that reduce the memory accesses, namely *subline\_mp*, *share\_lm\_cl*, *symmetry\_lm\_cl*, and *symmetry\_pf\_cl* (the top performing kernel that includes all optimizations). In Figure 6, we show the performance of *symmetry\_pf\_cl* with different configurations values for the batch numbers going: 1, 2, 4, ..., 32. The total number of memory accesses to projections (in float32 words) is  $N_{memProj} \approx 4 * nx * ny * nz * np$ (four accesses per voxel update by bilinear interpolation). The total number of memory accesses to the volume data is  $N_{memVol} = nx * ny * nz * np/nb$ , as explained in Section 3.1.3. Therefore, the

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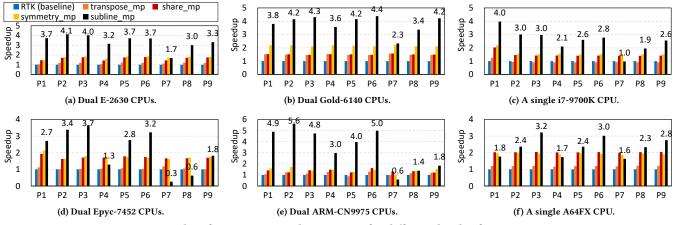


Figure 7: Speed up for OpenMP implementation for different levels of optimizations.

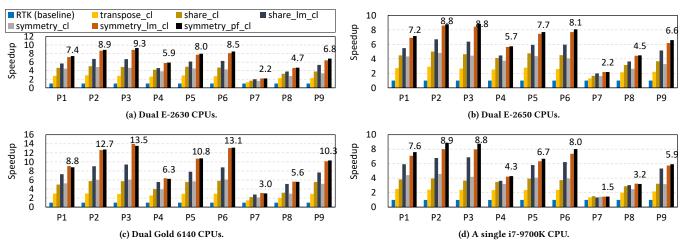


Figure 8: Speed up for OpenCL implementation (Intel CPUs) at different levels of optimizations.

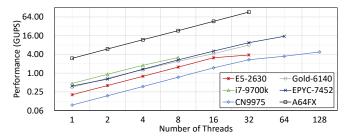


Figure 9: Scaling of the proposed back-projection with the number of threads for problem P5 ( $512 \times 512 \times 512 \Rightarrow 512^3$ ).

total number of memory accesses are

$$N_{mem} = N_{memProj} + N_{memVol} \approx (4 + \frac{1}{nb}) * np * nx * ny * nz$$

For a given image reconstruction problem, the variables np, nx, ny, and nz are fixed. Consequently, the performance is inversely

proportional to  $N_{mem}$ . When observing the performance in Figure 6, the performance behavior demonstrates this inverse proportional relation to equation  $N_{mem}$ .

In addition, we observe a performance gain when using a larger batch number for most of the image reconstruction cases. Since the performance of back-projection is bounded by the memory bandwidth, the proposed algorithm benefits from the reduced memory access when batching the projections. To simplify the parameters configuration, in the following experiments we report results for a fixed batch number of *32*.

## 4.4 Performance & Scalability on CPUs

This section reports the performance and scalability of the proposed algorithms. Figure 7 and Figure 8 show the performance of several back-projection kernels on different CPUs and image reconstruction problems as listed in Table 3. The levels of optimization of all evaluated kernels can be found in Table 2. We show speedup over the baseline, which is the multi-threaded and widely used

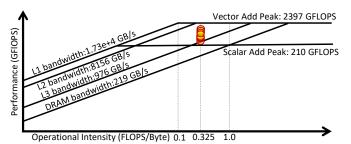


Figure 10: Roofline analysis of symmetry\_pf\_cl, provided by Intel advisor profiler [29, 39] on dual Gold-6140 CPUs. The problem size P1, P2, ..., P9 are shown as yellow dots.

Listing 4: Assembly snippet from *symmetry\_pf\_cl* OpenCL kernel with AVX2 vectorization.

```
0x177801a62b4 movsxd rdx, edx
2
   0x177801a62b7 vmovups ymm10, ymmword ptr [r8+rdx*4]
   0x177801a62bd vmovups ymm11, ymmword ptr
3
                                             [r14+rdx*4]
4
   0x177801a62c3 vsubps ymm11, ymm11, ymm10
   0x177801a62c8 vfmadd213ps ymm11, ymm1, ymm10
5
   0x177801a62cd mov rsi, rax
6
7
   0x177801a62d0 shl rsi,
                           0xc
   0x177801a62d4 add rsi, r11
```

back-projection implementation in the RTK library. The OpenMPoptimized *subline\_mp* and OpenCL-based *symmetry\_pf\_cl* achieve the highest performance in most of the cases, due to the collective use of optimizing techniques. The speedup of *subline\_mp* ranges from 0.3 to 5.6 and *symmetry\_pf\_cl* ranges from 1.5 to 13.1. Upon investigation, it became clear that this performance differential between OpenMP and OpenCL is due to the higher quality of vectorized code by the OpenCL compiler. Taking the *subline* kernel as illustrated in Section 3.1.4 for instance, the assembly snippet can be found in Listing 4. The quality of those vectorized codes (AVX2) is competitive to the hand-optimized ones that we tested. To sum up, the portable performance improvement of the proposed algorithms is due to better vectorization, improved data locality, and reduced memory traffic.

Figure 9 shows the scalability of the proposed back-projection algorithm (*subline\_mp*) on several CPUs (listed in Table 1). The number of threads is configured as the power of two and ranges between *1* to *128*. The performance on most CPUs scales linearly to the number of cores up to some point where we observe saturation of the memory bus.

## 4.5 Discussion

We show the speedup of several kernels in both Figure 7 and Figure 8 by comparing it with the baseline kernel. We observe the following: (I) OpenCL-based kernels outperform the OpenMP-based kernels due to the difference in the quality of the vectorized codes. The highest speed up among OpenMP-optimized kernels is almost 5.6×, in comparison to 13.5× for OpenCL-optimized kernels. (II) Prefetching is effective in improving the performance in several kernels: *symmetry\_pf\_cl* (kernel using prefetching) performs better than *symmetry\_cl* (kernel not using prefetching) across different CPUs

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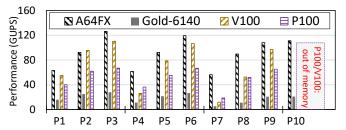


Figure 11: Performance comparison with GPUs at singleprecision (end-to-end image reconstruction). Problem P10 can not be solved on P100/V100 GPUs since it exceeds the capacity of device memory. For GPUs, we use the CUDA implementation in the RTK library.

and image reconstruction problem sizes. The performance gain is noticeable specially in Gold-6140 CPU, likely due to that CPU's large L2/L3 cache (as listed in Table 1). (III) The bilinear interpolation scheme (in Section 3.1.5) is very effective in improving the performance of back-projection kernels. It is clear that both share\_lm\_cl and symmetry\_lm\_cl (kernels using the subline bilinear scheme) perform better than share\_cl and symmetry\_cl (kernels not using the subline bilinear scheme), respectively. (IV) The optimization techniques such as shared reuse of hoisted variables and exploiting geometry symmetry (illustrated in Section 3.1) contribute to improving the performance of back-projection kernels. The kernels share\_cl, share\_lm\_cl, and symmetry\_cl outperform the kernel transposal\_cl that only transposes the projection and volume. (V) As shown in Figure 10, we display the Roofline model for the kernel of the top-performing kernel symmetry\_pf\_cl on dual Gold-6140 CPUs. The optimizations we introduce push the effective achievable bandwidth to be between the bandwidths of L3 and L2 caches.

4.5.1 **Comparison with GPUs.** We demonstrate the competitive performance of our back-projection implementation on the A64FX ARM CPU by comparing it to the high-end Nvidia P100 [35] and V100 [36] GPUs (see Figure 11). The RTK library implementation for GPUs is used for our comparison. RTK is one of the most widely used open-source library implementations on GPUs, to the author's knowledge. Our comparison includes the overhead of moving the projections from host memory to device memory and excludes moving the volume back to the CPU. Note that production-level GPU implementations of back-projection (including RTK) do not overlap the computation with moving the projections to the device. That is primarily because overlapping schemes would require complex alterations in the image reconstruction algorithm in addition to being ineffective when scaling the size of projections (i.e. the algorithm cannot function on partial projections).

To conduct a fair comparison, we perform all computations in single precision (without using mixed precision on GPUs or CPUs). Using the A64FX CPU, we can achieve better performance than Tesla P100/V100 GPUs on a wide range of image reconstruction problems. For fairness, we compare our work to RTK's CUDA implementation since RTK is one of the most used open libraries in both research and industry, though RTK's performance can be further optimized using the algorithms in [28]. Note that the implementation in [28] is not publicly available. The RTK implementation uses a double buffer method to store volume data (read data from a volume buffer and write the updated value to another buffer). Hence, the maximum capacity of the generated volume must be less than 8 GB. That is because the device memory is limited to 16 GB in P100/V100 GPUs, and a fraction of the device memory is also required to store the projections. It is important to mention that the A64FX can solve the bigger problems such as P10 ( $1024^2 \times 512 \Rightarrow 1300^3$ ) in Figure 11, however, the P100/V100 GPUs are restricted by their memory capacities.

To sum up, with the A64FX CPU we reach performance that rivals the top-performing GPUs when accounting for the unavoidable data movement overhead.

## **5 RELATED WORK**

Tomographic image reconstruction has been heavily researched in the past years. Target-specific hardware is often adopted to speed up the computation of back-projection. Swindell et al. [53] proposed a linear accelerator to optimize the back-projection for the first generation CT devices three decades ago. Wu et al. [62] used Application-Specific Integrated Circuits (ASIC) to speed up the back-projection algorithm. The authors in [8, 18, 51, 64] also employed an FPGA to tune the computation of the FDK algorithm. Several authors also use high-level synthesis methods [8, 18, 51, 64], e.g. OpenCL, to generate the fast back-projection kernels on FPGAs rather than using HDL language. Treibig et al. [54] employed the SIMD instruction set extensions (coded in assembly) to speed up the back-projection computation and achieved outstanding performance on CPUs, e.g. using four Xeon E7-4870 CPU to generate a volume of size 1024<sup>3</sup> and achieve the performance of 12 GUPS. Notably, such performance is less than a quarter of the peak performance on Intel CPUs. Furthermore, Treibig et al. took advantage of the gather load intrinsic to alleviate the pressure of memory traffic on accessing projections (e.g. access along line  $\bar{a}$  in Figure 2). However, the performance of the optimized kernel was still bounded by the memory accesses to update the volume data. Unlike their method, we can fully vectorize the back-projection and reduce the memory access for projection and volume data. In [20], Johannes et al. optimized the RabbitCT benchmark on the Intel Xeon Phi accelerator. Using a fixed-point DSP (Digital Signal Processor) platform, Liang et al. presented an optimized implementation of FDK and achieved state-of-art balance in cost and power consumption [27]. On several hardware accelerator platforms such as CPUs, GPGPUs, and Intel Xeon Phi, Serrano et al. [47] proposed a parallelized FDK implementation.

Xiao et al. [56] proposed a super-voxel technique to improve the data locality of the MBIR algorithm. However, super-voxels were restricted for parallel-beam based CT. Mert et al. [19] optimized an iterative image reconstruction algorithm and achieved petaflops performance on Summit supercomputer for parallel-beam CT systems, yet their solution is difficult to be applied to the current generation of CT devices (namely Cone-beam CT). In this work, we target Cone-beam datasets ( $7^t h$  generation CT) for which voxel-based back-projection is required. Lu et al. [28] proposed a highly optimized back-projection algorithm for out-of-core computations. Their implementation is more specific for CUDA, e.g. using texture memory. Furthermore, the projection computation in our algorithms is fully different from their approach, e.g. we use the  $3\times4$  projection matrix shown in Listing 1.

RTK library (the baseline of this work) is a widely used library for image reconstruction and provides implementations of backprojection kernels for both CPU and GPU architectures. Due to the complexity of overlapping schemes in back-projection, the unavoidable overhead of data movement between host and device may degrade the performance. Furthermore, the GPU memory is limited and thus, volume decomposition techniques are required to go out-of-core [4, 28].

## 6 CONCLUSION

We revisit the role of CPUs in image reconstruction, motivated by the considerations of cost, power, and space requirements. To use CPUs for compute-intensive medical image processing applications, this paper proposes performance portable back-projection algorithms and demonstrates the performance/scaling on a wide range of multicore processors. Our implementations benefit from the agnostic vectorization, improved memory access pattern, and reduced arithmetic computations. We also propose a bilinear interpolation algorithm to cache the sub-line values of projections for reducing the memory access and the arithmetic computations. Our results show that the proposed back-projection can achieve, on average, 5.2× speedup over the multi-threaded implementation of the most widely used library, on a wide variety of CPUs. We demonstrate the capability of using an ARM CPU (A64FX) to outperform high-end GPUs in the domain of CT, which is traditionally driven by GPUs.

## ACKNOWLEDGMENT

This work was supported by JSPS KAKENHI Grant Number JP21K17750. This work was partially supported by JST-CREST under Grant Number JPMJCR19F5; JST, PRESTO Grant Number JPMJPR20MA, Japan. We would like to thank Endo Lab at Tokyo Institute of Technology for providing computing resources. The author wishes to acknowledge useful discussions with Dr. Jintao Meng at Chinese Academy of Science (CAS).

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