

An Empirical Study of the Performance of the APL370 Compiler

WAI-MEE CHING, RICK NELSON AND NUNGJANE SHI

IBM T. J. Watson Research Center Yorktown Heights, NY. 10598 USA

ABSTRACT

The performance of a compiler is usually measured in terms of the execution efficiency of compiled code and the speed of compilation. For an APL compiler, we are also concerned about its relative performance with respect to the interpreter: C/I, the ratio of the speed of compiled code over interpretation. We give performance data on 4 groups of examples of moderate size: i) scalar style code where an interpreter does poorly and the C/I ratio is very high, ii) good APL style code where interpreter still does poorly due to inherent iterativeness or recursion, and the C/I ratio is high, and iii) good APL style code where the interpreter is very efficient on large data, and the C/I ratio is low, and iv) some particular primitives. These examples include neural net simulation, machine simulation, network routing, signal processing and mathematical computations. The APL370 compiler not only speeds up applications of iterative nature, but also gives good performance to codes utilizing APL's strength such as boolean selection and boolean data manipulation.

1. INTRODUCTION

A prototype copy of the APL370 compiler has been released on the APLTOOLS disk for IBM internal use. The APL370 compiler compiles an essential subset of APL, which includes recursion, directly into 370 assembly code. The compiled code can run independently of any interpreter or be called from APL2. This version does not yet include the option of directly generating vector instructions for the IBM 3090 vector facility (we shall release the vector version later). Hence, the performance data we discuss here is that of the released sequential version, not the experimental vector version we reported in [1]. Also, the interpreter we compare to is APL2 release 2 (it has no vector option). However, the compiler in the released form needs APL2 release 3 to run because it uses the package feature of release 3 to avoid possible name conflicts.

Since an APL interpreter provides excellent programming environment for APL program development, an APL compiler is not a replacement of the interpreter. The APL370 compiler is designed for execution efficiency, interpreter independent execution and ease of compilation. It is intended to help the following APL users:

- whose APL application program has an inherently iterative component,
- who need to write a module in APL implementing complex algorithms for a non-APL-based system where no APL interpreter is present during execution,
- who have no time, patience or experience to write their APL program in an elegant fashion so as to save interpretation time.

Despite current implementation limitations and remaining bugs in the compiler, we believe our compiler already can be of good help to many APL users to gain the speed and/or convenience of interpreter independent execution. Moreover, it is important to demonstrate that an efficient APL compiler is possible without imposing requirements alien to APL style programming and/or need to rely on other programming language processors for execution performance.

There are two components in the measurement of a compiler performance: First, the execution speed of the compiled code, and the second, the compilation time. We note that even though the second component is less important in comparison with the first, it is by no means negligible.

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This is because in real life a successful compilation is usually not the end of the story. People naturally asks if any change can make the compiled code run even faster. Since this question cannot be settled by running on the interpreter, recompilation of a modified program becomes necessary. Hence, slow compilation speed is tolerable but very slow compilation speed is intolerable.

As for a the measurement of compiled code efficiency, a simple way is to measure how much time is required for the execution of each primitive. This can be quickly seen as ineffective for measuring the overall performance. For an interpreter is likely to outperform a compiler on the code for many/most primitives, but still be beaten by a compiler on whole examples. On the other hand, we can always cook up examples of triple loops which occur rarely in real life APL programs to show a speedup of, say 200 times. The real performance of an APL compiler is the speedup it can provide on codes it is likely to encounter in real life applications. Since there is no good way to assemble a sample of APL programs which all agree as representative, we decided to use a collection of varying characteristics to measure the performance of the compiler. Hence we called this an empirical study of the compiler performance.

Seasoned APL programmers typically develop an intuitive criterion as whether an APL program is of *good APL style*. Usually, such APL programs execute faster (under interpreter) than equivalent but less well thought out programs. Our informal definition is that an APL program is of good APL style if it uses high level primitives and avoids loops whenever possible. We note that such a definition is only possible for a language of very high level where usually exist very different implementations of any particular computation task.

An APL compiler is of limited value if all it can speedup (with respect to the interpreter) are FORTRAN style programs. It must be able to help programs of good APL style in performance so that the efficiency of the compiled code is better than interpretation and is comparable to that produced by an optimizing FORTRAN compiler from corresponding FORTRAN programs. In the case of the APL370 compiler, this is indeed true and the compiled code actually executes faster then FORTRAN if powerful APL features such as bit-manipulations are involved. The compiled code in general executes no slower than the interpreter in stand-alone mode where no copying of data to and from an interpreter environment is involved. In order to prove our point, we collected a group of examples, some are simple and some are from users' real life applications. We group them into four different categories and measured their performance. All the performance data is obtained from running on a single processor scalar machine of IBM 3090 model. The numbers are in cpu seconds for the table of compilation and in *cpu milliseconds* for the tables of execution time of compile code.

One important feature of compiler performance not discussed in this paper is that of space utilization. We only mention that the APL370 compiler currently does not provide a garbage collector at run-time, but it does have features to save space as to minimize the need of a garbage collector.

Finally, we remark that the current version of the APL370 compiler does not have any particular optimization other than in certain isolated but frequently occurring cases like $+\rho$, $\rho\rho$, [...], ... ρ 0. It is not that we regard such well known optimization technique like 'drag-along and beating' to combine primitive operations as not desirable; rather, we feel this is less important than the efficient implementation of certain frequently used primitives such as boolean compress when constrained with limited resource. We certainly will include streaming in our newer version of the compiler later to further improve the compiler performance.

2. COMPILATION TIME

We divide the compilation time in that of the front end and that of the back end. The front end time includes parsing, data flow analysis, type shape analysis and data dependency analysis. The back end time includes generation of 370 assembly code from the parse trees produced by the front end and the actual writing of code characters onto the output file. We can see that the back end spends more time than the front end. Part of the back end time is in writing files (about a quarter), and this part can be substantially reduced when we later use the block write feature of APL2. No discernible improvement can be expected of the front end where no output process is involved, unless substantially more efficient new data analysis algorithms become available. On the other hand, since both the compiler is written entirely in APL (VS APL) and essentially confined to the compilable subset, a speedup naturally will follow when we compile the compiler. The back end compilation speed depends very much on the density of the source code defined as the ratio of corresponding assembly code and its APL source code (a scalar style code is much less dense than an equivalent program using many high level primitives, i.e. code of good APL style). Hence, the usual line/sec. measure of compilation speed used in FORTRAN and PASCAL compilers is not really comparable, just as an APL programmer's productivity cannot be measured with the same lines/day accounting used for FORTRAN programmers.

Compilation Time

Example	Fns	Lns	Front	Back	Total	Lns/sec
PRIME4	1	10	0.5	2.11	2.61	3.84
QSORT	2	25	0.99	3.36	4.35	5.75
HEBB	2	63	2.07	9.85	11.92	5.28
PRIME5	1	6	0.37	2.11	2.48	1.14
PRIME3	1	7	0.44	3.23	3.67	
QSOR	1	4	0.29	3.23	3.52	
CHOLESK	2	15	0.96	5.38	6.34	
WAIMEE	4	51	2.06	9.27	11.33	
DAVID	1	25		12.1	14.18	1.76
COLORK	2	30		11.78	13.85	2.17
FFOT	1	41		28.93	32.1	1.28
BFRANK	1	15		5.38	6.09	2.46
SSOLVE	6	85		37.5	42.8	1.99
NIH	4	124		19.12	24.49	5.06

We note that back end time dominates and it roughly corresponds to the amount of 370 assembly code generated. Hence, the lines per second measure confirms our intuition of the denseness of a piece of APL code.

The compilation speed of the APL370 compiler is much faster than other APL compilers we are aware of. For example, it is mentioned in Wiedmann [2] that it takes 34 hours for the STSC's compiler to analyze and speedup an electronics design application. In our case, we recommend users to compile as large a unit as possible because our compilation time is not prohibitively expensive and compiled code is mostly no worse than the interpreter. This saves the effort to pinpoint a function which is an execution bottleneck for compilation.

Even though the issue of compilation speed is less important than that of the execution speed of compiled code, it is not some unimportant issue to be brushed aside. Indeed, for mature compiler technology on scalar languages such as FORTRAN and PASCAL where the difference between the execution speeds of compiled code produced by different compilers is less pronounced, compilation time is a major criterion for compiler performance. While APL programmers use the interpreter to developed their programs, after the code is compiled, they inevitably wonder whether they can do better. And this question can only be answered by recompilation and execution of a changed program. Prohibitive compilation cost will discourage such experimentation.

3. EXECUTION TIME OF COMPILED CODE

We group our examples into four groups: i) sequential style code, ii) APL style code with strong iterative or recursive components, iii) good APL style code already efficient on the interpreter and iv) some primitives. The speedup our compiler can provide are certainly different for each of these groups. An APL compiler can remove the severe performance penalty the interpreter exacts from naive APL users on their scalar style code; but it is more important for an APL compiler to reassure its users that they would not be penalized when writing good APL style code by the compiler. That is, the compiler should not perform worse than the interpreter on good style APL code. We also remark that even though the speedup the compiler provides on group i) is greater than that on other groups, the compiled code actually performs a bit better when an APL program is written in good APL style (compared with its scalarized counter part). So there is no incentive to write scalar style code when using the compiler. On the contrary, soon there should be strong incentive to write good style APL code when we make the vector code version of the compiler available (data on experimental results have already been presented in [1]). And work on progress on automatic multi-tasking of APL programs has already convinced us that to write good APL style code is even more important for the parallel execution on IBM 3090 style multi-processors since the use of high level primitives in APL provides suitable granularities for parallel processing. The execution time excludes output display time (both for interpreter). The compiled code time is measured in stand alone mode which is indistinguishable from the time measured when called from APL2 except the example BFRANK where there is a noticeable difference most likely due to getting input data from its APL2 environment to the compiled module (We are looking into this peculiar problem for very large character inputs). BFRANK is also the only workspace in our collection where for certain inputs the compiled code is running slightly slower than the interpreter. We have evidence that this is not due to the fact we didn't do combining on primitives. We suspect the reason to be that some of our primitives are still not implemented as efficiently as possible.

Only PRIME4 has a hand translated FORTRAN for comparison. This is because PRIME4 is of FORTRAN style (hence its translation is straightforward). It would be interesting to see how compiled APL code compares with FORTRAN on other examples. Unfortunately, to write the FORTRAN counterpart for other examples is not easy task and we do not have access to automatic translation at present.

Group I

PRIME4				
Ν	Interpret.	Compiled	Speedup	FORTRAN
10	10	0	-	0
100	212	4	53	3
500	2069	35	59.1	22
1000	5544	89	62.3	58
2000	14908	233	64	148
5000	55614	847	65.6	522
QSORT size 75a 75b	Interpret 88	ter Comp 0.0 0.1	63 ¹ 1	eedup 39.9 44.05
HEBB size L 2x6 R 73	Interpret	ter Comp 1.1		eedup 20.49

We note that PRIME4 is not exactly a scalarized version of PRIME5 below because it has boolean short circuit testing built into it. Hence it is more efficient algorithmically, but under interpreter it is slower because its' scalar style. QSORT is the scalar version of quick sort, which is the literal translation of the PASCAL program 2.10 on page 79 of Wirth [3] while QSO is basically an APL one-liner. HEBB is a program in the area of neural network. WAIMEE is a program doing simulated annealing for machine design. We only used 100-th size of real input because the interpreter takes too long time. It is these type of programs a compiler becomes a necessity since interpretation takes too long while rewrite in FORTRAN consumes valuable time of the designer. SSOLVE is the mathematical kernel of a package for interactive chip design available from IBM (this is the same program mentioned in [2] as an application in electronic design). Specifically, SSOLVE solves sparse matrix by the Gauss elimination methods.

NIH is a code from the National Institute of Health. The main function calls a function OPTD in a loop. The function OPTD is very iterative (its' flow graph is shown in the Appendix) and calls other 2 functions, both having only one basic block, inside loops. This is the only code which is not compiled as given. We made three modifications. First, the code was written in APL2 using nested arrays. We wrote an equivalent VS APL version which does not use nested arrays. Second, we rearranged parameters to the main function so that all integer inputs are grouped in the left argument and all floating point inputs are grouped in the right argument. Third, we insert code to initialize several variables a certain size to save heap space. We note that the vectors created during the execution of the code are rather small, so vector facility of IBM 3090 and vector code does not give much help in this example.

Group II

PRIME5			
N	Interpreter	Compiled	Speedup
10	7	1	7
100	85	8	10.6
500	549	87	6.3
1000	1250	248	5.04
2000	2903	706	4.11
5000	8944	2906	3.08
PRIME3			
N	Interpreter	Compiled	Speedup
1000	14	1.47	9.52

QUICKSOR			
size 100	Interpreter 24	Compiled 2.14	Speedup 11.21
CHOLESKY size 100	/ Interpreter 4	Compiled 0.15	Speedup 26.67
WAIMEE size L 2x12 R84x84	Interpreter 106179.2	Compiled 8065.61	Speedup 13.12
		Group III	
DAVID size L 12x2 R 62x4	Interpret 29	cer Compiled 2.3	Speedup 12.6
COLORK size L33/R33× L64/R64× L100/R10	33 117	cer Compiled 14.4 79.13 293.82	Speedup 8.13 4.52 3.38
FF0T size 8×8 32×32 128×128 1024×102	7 14 3 27	cer Compiled 0.37 1.08 3.67 30.4	Speedup 18.91 12.96 7.36 3.22
BFRANK size L1041x8/ L1041x4/ L 804x4/ L 804x4/ L 823x8/ L 886x8/ L 798x8/ L 798x8/ L 665x8/ L1139x14 L 701x2/ L 701x4/	'R1×8 5 'R4×4a 16 'R4×4b 15 'R4×4 12 'R4×8 16 'R1×8 4 'R4×8 14 'R4×8 12 'R4×8 14 'R4×8 12 'R+×8 14 'R 4×8 12 +/R1×14 7 'R 2×2 3	cer Compiled 4.4 8.37 8.43 5.61 14.4 3.7 11.96 9.81 8.29 1.49 3.27	Speedup 1.14 1.91 1.78 2.14 1.11 1.08 1.17 1.22 0.84 2.01 2.14
SSOLVE size L162xR18 (25 equa	30 117.45	cer Compiled 5 20.37	Speedup 5.76
NIH Outer it 20 90	ers Interpre 384 792168	eter Compileo 73.27 153498.8	

We have seen from the above that a compiler which is not very efficient on computation intensive primitives has no advantage over the interpreter in good APL style code when data arrays become quite large. Hence, it is very important for a compiler to generate very efficient code for computation intensive primitives. We give some sample below:

Group IV

Function		Z≁⊞A		
size	Interpreter	Compiler	Speedup	
10x10	10	2	5	
50x50	296	128	2.31	
100x100	2307	1019	2.26	
Func	ction	Z ←A 🗄 B		
size	Interpreter	Compiler	Speedup	
10x10	6	2	3	
50x50	290	137	2.12	
100×100	2331	1090	2.14	
Func	ction	Z + A+	.×B	
	ction Interpreter		• •	
size			• •	
size 10x10	Interpreter	Compiled	Speedup	
size 10x10 50x50	Interpreter 4	Compiled 1	Speedup 4	
size 10x10 50x50 100x100	Interpreter 4 216	Compiled 1 100	Speedup 4 2.16 2.18	
size 10x10 50x50 100x100 <i>Func</i>	Interpreter 4 216 0 1770	Compiled 1 100 812 <i>Z←A</i> +	Speedup 4 2.16 2.18 . <i>÷B</i>	
size 10x10 50x50 100x100 <i>Func</i> size	Interpreter 4 216 0 1770 ction	Compiled 1 100 812 <i>Z←A</i> +	Speedup 4 2.16 2.18 . <i>÷B</i>	
size 10x10 50x50 100x100 <i>Func</i> size 10x10	Interpreter 4 216 1770 ction Interpreter	Compiled 1 100 812 Z+A+ Compiled	Speedup 4 2.16 2.18 . <i>÷B</i> Speedup	
size 10x10 50x50 100x100 <i>Func</i> size 10x10 50x50	Interpreter 4 216 1770 ction Interpreter 5	Compiled 1 100 812 Z+A+ Compiled 2	Speedup 4 2.16 2.18 .*B Speedup 2.5	

4. CONCLUSION

We have presented performance data of the APL370 compiler on a variety of examples. We see that the compiler not only speedup scalar type code, but also provides good performance on most good style APL code. Moreover, the compilation time is reasonable and can be further reduced with improved output method.

Acknowledgement We thank Ralph Linsker, David Stein and Dan Milch of IBM T.J. Watson Research Center, Leo Maissel of IBM Poughkeepsie, Dan Chazan of IBM Israel Scientific Center, Bill Frank of IBM Burlington, David Zein of IBM East Fishkill and Richard Simon of the National Institute of Helath for their interest in the APL370 compiler and for providing us interesting real life examples.

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[3]N. Wirth, Algorithms+ Data Structures= Programs, Prentice Hall, 1976.

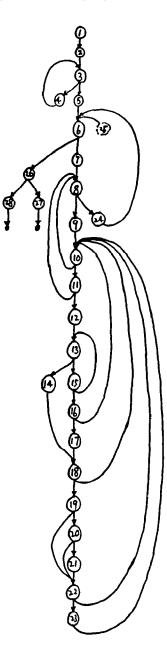
APPENDIX

<pre>∇ P+PRIME5 M;I;J [1] ∩ Print first M primes. □IO=1 [2] P+Mp0 [3] P[I+J+1]+2 [4] WHILE0:+(I≥M)/0 [5] W1:+(∨/0=(([I*0.5)+P) J+J+2)/W1 [6] P[I+I+1]+J [7] +WHILE0 </pre>
<pre>∇ P+PRIME4 M;I;J;IROOT [1] P+Mp0 A print first M primes [2] P[I+J+1]+2 A □IO=1 [3] WHILE0:+(I≥M)/0 [4] IROOT+[I*0.5 [5] LOOPB:K+1 [6] J+J+2 [7] LOOPA:+(0=P[K] J)/LOOPB [8] +(IROOT≥K+K+1)/LOOPA [9] P[I+I+1]+J [10] +WHILE0</pre>
$\nabla \qquad \nabla PZ + PRIME3 X; NP; E; S$ [1] $P + X \rho 1$ [2] $E + (S + \lfloor X * 0.5) \rho P[1] + 0$ [3] $RPT : + (S < NP + P \iota 1) / END$ [4] $P + P > X \rho (-NP) + 1$ [5] $E[NP] + 1$ [6] $+ RPT$ [7] $END: PZ + (E/\iota S), P/\iota X$ ∇
∇ Z+QSO V;B;W [1] → (1< ρ V)/S n □IO+1 [2] Z+V [3] →0 [4]S:Z+(QSO(~B)/W),V[1],QSO(B+W≥V[1])/W+1+V ∇
<pre>∇ L+CHOLESK A;N;I;C;J [1] ACholesky decomp of sym. posi.def.matrix [2] N+(~1+((1+8×pA)*0.5)) ±2 [3] L+A[1]*0.5 [4] I+1 [5] S2:+(N<i+1+j+i) +s2<br="" 0="" [6]="" [7]="" [8]="" c+l="" l+l,c,(a[l(i×i+1)±2]-c+.×c)*0.5="" ltr1(j+(l((i-1)×i)="" ±2)+a)="">∇</i+1+j+i)></pre>

```
\nabla L+A0 LTR1 X0;N;I;A;X
[1] ACalc. elements below the main diagonal [1] <u>LEV+RTMP[1 2 ;]</u>
[2] X+,X0
[3] A+,A0
[4] S1: N \leftarrow (-1 + ((1 + 8 \times \rho A) * 0.5)) \div 2
[5] I+1
[6] L \leftarrow X[1] \div A[1]
[7] S2: \rightarrow (N < I + I + 1) / 0
[8] L \leftarrow L, ( \div A[J+I]) \times X[I]
       -A[(J \leftarrow [I \times J \div 2) + iJ] + \cdot \times (J \leftarrow I - 1) + L
[9] →S2
         Δ
         \nabla C+ZAP COMADJ A; I; J; N; M
      N+1+pA A□I0+0
[1]
      M+<sup>−</sup>1+ρA
[2]
[3]
        C+(2pN)p0
[4]
        T+0
[5] \underline{L}0:C[J;J]+1+C[J;J+A[;I]/1N]
[6]
       \rightarrow (M > I + I + 1) / L0
[7]
      \rightarrow (0=ZAP)/0
[8]
         C \leftarrow C \times \sim A
         Δ
∇ R+MEM COLORK A; B; C; IO; JO; NS; W1; W2; W3; Y; Z
[1] \underline{X} \leftarrow C1 \leftarrow 1 COMADJ A
[2] CO+,C1
[3] \quad CO[(1+B) \times \iota B \leftarrow 1 + \rho A] \leftarrow 0
[4] C \leftarrow (\rho A) \rho C 0
[5] NS \leftarrow (1 + \rho C) \rho 1
\begin{bmatrix} 6 \end{bmatrix} \underline{L}1: \rightarrow (0 \ge B \leftarrow \lceil /, C) / \underline{L}2
[7] B + (,C) \, \iota B
[8] J0 + (10)\rho(1+\rho C) | B
[9] I0 \leftarrow (10) \rho LB \div 1 + \rho C
[10] Y + A[J0;] + 2 \times A[I0;]
[11] W1+(Y=1)/ιρΥ
[12] W_{2+}(Y=2)/1\rho Y
[13] W3+(Y=3)/1pY
[14] Z \leftarrow (\sim A[;J0]) \times + /A[;W1]
[15] A[;I0]+A[I0;]+A[J0;] A[J0;]
[16] C[W3;W3] + C[W3;W3] - 1
[17] C[W2;W1] + \diamond C[W1;W2] + C[W1;W2] + \sim A[W1;W2] [12] CT+1
[18] C[;I0]←C[I0;]←C[I0;]+Z×∼A[I0;]
[19] MEM[(MEM=MEM[J0])/\iota\rho MEM] \leftarrow MEM[I0]
[20] C[I0;W1]+C[W1;I0]+C[J0;]+C[;J0]+A[J0;]
        +A[;J0]+NS[J0]+0
[21] →L1
[22]L2:R+MEM
      V
```

```
∇ MM+RTMP DAVID M; CROSS; J∆; L∆; P∆; K∆; MID
 \begin{bmatrix} 2 \end{bmatrix} SM+ 2 0 +RTMP
 [3] <u>MM+M</u>
 [4] \underline{MM}[;1] \leftarrow (\underline{MM}[;1] \neq 0) \times \underline{LEV}[1;2]
[5] \rightarrow ((1=1+\rho \underline{LEV}) \vee \wedge / \underline{LEV}[1;2] = \underline{LEV}[;2]) / 0
 [6] \quad \underline{CUMSM} + |SM[;1] \times 2 - SM[;2]
[7] <u>FΔ</u>← 2 4 ρ0
[8] <u>L∆</u>+1[+/<u>M</u>[;4]∘.≥<u>LEV</u>[;1]
[9] \underline{MM}[\underline{T\Delta}/\iota1+\rho\underline{M};1]+\underline{LEV}[(\underline{T\Delta}+\underline{M}[;1]\neq 0)/\underline{L\Delta};2]
 [10] \underline{CROSS} \leftarrow (((1 + \underline{T\Delta}), 0) \land \underline{L\Delta} \neq (1 + \underline{L\Delta}), -1 + \underline{L\Delta}) / \iota \rho \underline{L\Delta}
[11] <u>J∆</u>+''ρρ<u>CROSS</u>
 [12]\underline{LQQP}: \rightarrow (\underline{J}\underline{\Delta}=0) / 0
[13] <u>K∆+CROSS[J∆</u>]
 [14]\underline{RP}:\underline{MID}+''\rho\underline{LEV}[\underline{L}\Delta[1+\underline{K}\Delta]+(\underline{MM}[\underline{K}\Delta;4]) \geq
                   \underline{MM}[\underline{K}\Delta+1;4]);1]
[15] \underline{P}\Delta + (|\underline{M}\underline{M}[\underline{K}\Delta;4] - \underline{M}\underline{I}\underline{D}) * |\underline{M}\underline{M}[\underline{K}\Delta;4] - \underline{M}\underline{M}[1 + \underline{K}\Delta;4]
[16] \underline{L}\Delta[1+\underline{K}\Delta]+\underline{L}\Delta[1+\underline{K}\Delta]+\times\underline{L}\Delta[\underline{K}\Delta]-\underline{L}\Delta[\underline{K}\Delta+1]
 [17] \underline{F}\Delta[;1] + \underline{L}\underline{E}\underline{V}[\underline{L}\Delta[1+\underline{K}\Delta];2],0
 [18] \underline{F}\Delta[; 2 3 4] + (2 3 \rho \underline{M}\underline{M}[\underline{K}\Delta; 1+13])
                   (2\rho \underline{P}\Delta) \circ . \times - /\underline{M}\underline{M}[1 \ 0 \ +\underline{K}\Delta; 1+\iota3]
 [19] \underline{M}\underline{M} \leftarrow \underline{M}\underline{M}[\iota \underline{K}\Delta;], [1] \underline{F}\Delta, [1](\underline{K}\Delta, 0) + \underline{M}\underline{M}
 [20] \underline{T}\Delta 1 + (\underline{CUM}SM \ge \underline{K}\Delta) \ 1 \ \mathbf{A} \ \underline{W}AS \ \underline{T}\Delta \ ****
[21] SM[\underline{T}\Delta 1; 1] + SM[\underline{T}\Delta 1; 1] + 1 + SM[\underline{T}\Delta 1; 2] = 1
 [22] \rightarrow (\underline{L}\Delta[1+\underline{K}\Delta]\neq 1[+/\underline{L}\underline{E}V[;1]\leq \underline{M}\underline{M}[\underline{K}\Delta;4])/\underline{R}\underline{P}
 [23] <u>J∆+J∆</u>-1
 [24] →<u>LOOP</u>
                 V
           \nabla E+A1 BFRANK A2; I; IT; ROW; CT; A2V
 [1] A Bill Frank's version, 9/87
 [2] \rightarrow (\sim' *' \epsilon A2)/L0 \cap Branch if no * in arg
 [4] A2V+,A2
 [5] A2V[(,A2='*')/ıρA2V]+' '
[6] A2←(ρA2)ρA2V
 [7] L0: + (' ' \epsilon A2)/L1 A Branch if there are
 [8] E \leftrightarrow \sqrt{A1} = QA2 \cap A do simple inner prod \rightarrow 0
 [9] →0
 [10]L1:I+1<sup>1</sup>+pA1 A Indices of data
 [11] ROW+1+pA2 A Loop counter & limit
 [13] E \leftarrow (1 \leftarrow \rho A1) \rho 0 \cap A Initialize results
 [14]L2:IT + (' ' \neq A2[CT;])/I
 [15] E \leftarrow E \lor A1[;IT] \land = A2[CT;IT] afind the match
 [16] \rightarrow (ROW \ge CT \leftarrow CT + 1)/L2
           V
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 \begin{array}{c} \forall \ W + FFOT \ X0; Z; X1; I; R; U; RO; X \\ [1] & R \ COMPUTES \ FFT \ OF \ A \ REAL \ FN \ WITH \ 2*N \ PTS \\ [2] & R \ INVERSE \ IS \ FTI \ (\rhoW) = (2, 1+(2*N)-1) \\ (2 & \phi X0) \neq f \ 2 & \phi x0) / 0 \\ [6] & X+((RO+(\rho X0)), 1) \rho X0 \\ [7] & Z+X[ u \ R^{\pm}2; ], X[ R+u \ (R+u \ (1+\rho X) \pm 2) \pm 2; ] \\ [8] & X+(X[ R+u R; ], X[ (3xR) \pm 1R+u \ R^{\pm}2; ]) \\ [9] & Z+Z+ \times 2 \ 2 \ \rho \ 1 \ 1 \ 1 \ 1 \\ [10] & X+X+ \times 2 \ 2 \ \rho \ 1 \ 1 \ 1 \ 1 \\ [11] & W+(R\rho \ 1 \ 0) \setminus ((R, 2) \pm 2), (R\pm (0.5\times R) \pm Z[; 1] \\ [12] & W[ \ 1+2\times u R; 1] + W[ \ 1+2\times u R; 1] + X[ u R; 1] \\ [13] & W[ \ 1+2\times u R; 3] + W[ \ 1+2\times u R; 3] - X[ u R; 1] \\ [13] & W[ \ 1+2\times u R; 3] + W[ \ 1+2\times u R; 3] - X[ u R; 1] \\ [14] & W[ \ 2\times u R; 2] + -X[ u R; 2] \\ [15] & X+((R+u(0.5\times R)), 0) + X \\ [16] & Z+((2\times R) \rho \ 1 \ 0) \setminus ((R, 0) \pm Z), R\pm Z[; 1] \\ [17] & Z[ \ 1+2\times u R; 3] + Z[ \ 1+2\times u R; 3] - X[ u R; 1] \\ [18] & Z[ \ 1+2\times u R; 3] + Z[ \ 1+2\times u R; 3] - X[ u R; 1] \\ [19] & Z[ \ 2\times u R; 2] + -X[ u R; 2] \\ [20] & U+ \ 2 \ 1 \ 0 \ 002\times (-(-(1+u 1+RO \pm 2) \pm RO)) \\ [21] & J+3 \\ [22] & +(1=1+\rho Z) / 0 \\ [23] & X+Z[ \ 1+2\times u R; ] \\ [24] & Z+Z[ \ 2\times u R; ] \\ [24] & Z+Z[ \ 2\times u R; ] \\ [25] & Z+(\rho X) \rho U[ \ 2; 1+(LRO \pm 2*I) \times \ 1+u [1+2*I-1] \\ [26] & LO1: X+X, 0 \ 1 \ + \phi X \\ [27] & Z+Z, -0 \ 1 \ + \phi Z \\ [28] & X1+X\times (\rho X) \rho U[ \ 2; 1+(LRO \pm 2*I) \times \ 1+u [1+2*I-1] \\ [29] & X+X\times (\rho X) \rho U[ \ 2; 1+(LRO \pm 2*I) \times \ 1+u [1+2*I-1] \\ [29] & X+X\times (\rho X) \rho U[ \ 2; 1+(LRO \pm 2*I) \times \ 1+u [1+2*I-1] \\ [20] & Z+0 \ 0 \ \rho 0 \\ [33] & X1+X1+W[ \ 1+2\times u R; ], -0 \ 1 \ + \phi W[ \ 2\times u R; ] \\ [34] & X+X+W[ \ 2\times u R; ], -0 \ 1 \ + \phi W[ \ 2\times u R; ] \\ [35] & W+(2 \ 1 \ [\rho X) + 0) \ \rho U \\ [36] & W[ \ 1+2\times u (1+\rho W) \pm 2; ] + X1[ u (1+\rho W) \pm 2; ] \\ [37] & X1+((l 0.5\times 1+\rho W) \ 0) \ + X \\ [40] & X+X1 \\ [41] & I+I+1 \\ [42] & R+LR \pm 2 \\ [43] & +(0=1+\rho X) / 0 \\ [44] & +LO1 \\ \hline \forall V \\ \end{array}
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