
FOREWORD

Special Section on Parallel and Distributed Computing and Networking

Welcome to the Special Section on Parallel and Distributed Computing and Networking. This special section was planned to publish high quality papers reflecting the recent research activities in the fields of parallel/distributed computing and networking, such as parallel/distributed algorithms and their applications, innovative hardware and parallel/distributed architectures, distributed systems and networking, and software systems and fundamental technology. This special section brings together high-quality and timely papers on the recent progress in the interdisciplinary area of parallel and distributed computing and networking. These papers include the extended versions of conference papers presented at the International Symposium on Computing and Networking (CANDAR'17), in addition to brand-new contributions from our IEICE members.

The submitted papers to this section were 20 papers and 3 letters, which were from four countries: 13 from Japan, 4 from China, 4 from Korea, and 2 from Taiwan. Through the fair and strict reviewing process by expert reviewers, 10 papers and 2 letters were finally accepted for publication. It was an extremely difficult task to select papers to be published because there were so many excellent and interesting submissions.

It is my honor to serve as the guest editors-in-chief of this special section. It is the result of hard and excellent work of the guest associate editors. Especially, we could not reach our goal without a hard work of two Guest Editors, Dr. Michihiro Koibuchi and Dr. Fukuhito Ooshita. We would also like to express our sincere appreciation to all authors for their valuable contributions, and to all external reviewers for their cooperation and contributions.

Special Section Editorial Committee Members

Guest Editors:

Michihiro Koibuchi (NII), Fukuhito Ooshita (Nara Institute of Science and Technology)

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Akihiro Fujiwara (Kyushu Institute of Technology), Guest Editor-in-Chief

Akihiro Fujiwara (*Member*) is a professor of Department of Computer Science and Electronics, Kyushu Institute of Technology. He received the B.E. degree in Information and Computer Science from Osaka University in 1993. He also received the M.E. and Ph.D. degrees in Information Science from Nara Institute of Science and Technology (NAIST) in 1995 and 1997, respectively. He has served as program committee or organizing committee chairs of many international conferences, and served on the editorial board of the IEICE Transactions on Information and Systems. His research interests include parallel algorithms and algorithms for natural computing. He is a member of IEEE Computer Society.

