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## FOREWORD

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### Special Section on Parallel, Distributed, and Reconfigurable Computing, and Networking

Welcome to the Special Section on Parallel, Distributed, and Reconfigurable Computing, and Networking. This special section was planned to publish high-quality papers on the recent research activities in the fields of parallel, distributed, and reconfigurable computing, and networking: innovative hardware, software, architectures, algorithms, applications, and fundamental technology for parallel, distributed, and reconfigurable computing, and networking. This special section brings timely papers together in the interdisciplinary area of parallel, distributed, and reconfigurable computing, and networking. These papers include the extended versions of conference papers presented at the International Symposium on Computing and Networking (CANDAR).

I would like to express the deepest appreciation to all who have helped to bring about this special section. In total, 18 papers and two letters were submitted from five countries. Then, 11 papers and two letters were accepted through the strict reviewing process by peer reviewers. I greatly appreciate the excellent job of three Guest Editors, Dr. Masahiro Iida, Dr. Shinya Takamaeda, and Dr. Toshihiro Yamauchi. I also thank the authors for their contributions to this special section and the reviewers for their comments and recommendations.

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Fukuhito Ooshita (Nara Institute of Science and Technology), Guest Editor-in-Chief

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**Fukuhito Ooshita** (*Member*) received the M.E. and D.I. degrees in computer science from Osaka University in 2002 and 2006. He had been an assistant professor in the Graduate School of Information Science and Technology at Osaka University during 2003 to 2015. He is currently an associate professor of Graduate School of Information Science, Nara Institute of Science and Technology (NAIST). His research interests include parallel algorithms and distributed algorithms. He is a member of ACM, IEEE, IEICE, and IPSJ.

