

Chopping-Out-Of-band (COOB) for reducing ripple in chopper amplifiers

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Abstract: This paper introduces a method named Chopping-Out-Of-band (COOB), which can suppress ripple in chopper amplifiers. By choosing a suitable chopping frequency (f_{chop}) located in $GBW < f_{chop} < NLBW$ (No-Load Bandwidth), the ripple will be suppressed rather than amplified and the effect can be improve further by an extra filter. The COOB technique was employed in instrumentation amplifiers as examples and the extra filter was designed to passive filter. These chopper amplifiers were simulated on UMC 0.18 um technology with the help of Cadence SpectreRF. Simulation results showed that the ripple was suppressed by $-93.4\,\mathrm{dB}$ in Power Spectral Density (PSD) analysis without additional power consumption.

Keywords: chopper-stabilized, chopping-out-of-band, offset, 1/f noise, ripple reduction

Classification: Integrated circuits

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1 Introduction

In CMOS technology, offset and 1/f noise have become the main circuit errors at very low frequency. Dynamic chopping technique is widely used for cancelling the offset and 1/f noise in high precision CMOS amplifiers. The offset and 1/f noise in chopper amplifiers are modulated to a higher frequency while the signal is demodulated on the baseband. However, chopping also brings imperfections such





as ripple and gain losing [1]. It has been reported that the ripple can achieve 48 mV at the output of the amplifier [2], undoubtedly this is unacceptable. There have been many papers which are reported in recent years show different ripple reduction techniques [2, 3, 4, 5]. On the other hand, in [1], the author describes that the chopping frequency (f_{chop}) is limited by finite bandwidth and delay introduced by the main amplifier. In this paper, we point out that the speed of chopping is not influenced by the load capacitances (C_L) in the chopping nodes. As a consequence, a ripple reduction technique named COOB is proposed, which sets the f_{chop} locate in the outside of GBW so that the ripple can be suppressed. Furthermore, an extra passive filter is employed to enhance the suppression. The COOB technique would not introduce extra power consumption and open-loop gain losing.

2 Proposed COOB

The operational transconductance amplifier (OTA), which is used as the first stage in instrumentation amplifiers, provides the main offset and 1/f noise. As a consequence the chopping is usually implemented on OTA, which is shown in Fig. 1. The input signal (a) will be modulated to (b) and then amplified (c). The speed of chopping is limited to that whether the signal (b) could be amplified smoothly. It is worth noting that some load capacitors (Fig. 1) or compensation capacitors (Fig. 4) appeared at the chopping nodes so that we must take them into account.

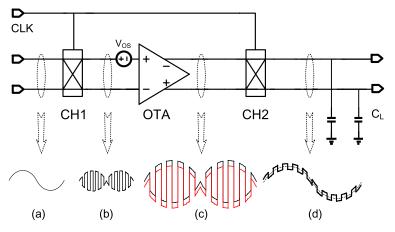


Fig. 1. The chopping theory.

As Fig. 2(a) describes, the OTA is amplifying a DC signal. The red line means high voltage while the blue means low voltage. At the moment when clock changes, shown in Fig. 2(b) and (c), the C_L remains unchanged while the OTA changes to its opposite state. That means the speed of chopping is not limited by the coaction of C_L and OTA but limited by the OTA itself. One step further, the f_{chop} should be set up to lower than the no-load bandwidth (NLBW) in order to get a maximum gain. The no-load ac response of OTA is described in Fig. 3.

With the help of modulators, offset will be modulated only once and then appears at the output (Fig. 1), this is the ripple. It is obviously that ripple will be suppressed by C_L . The ac response with C_L and without C_L are given in Fig. 3(b). The f_{chop} is set up to:





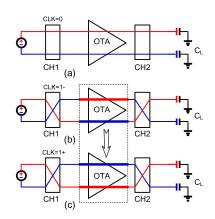


Fig. 2. Process of clock changing in the chopper amplifier.

$$GBW < f_{chop} < NLBW. (1)$$

The ripple is suppressed rather than amplified. Meanwhile, this choice of f_{chop} makes that a maximum gain is obtained. In general, the C_L are compensation capacitors such as C_{C1} in Fig. 4, by which the GBW of the OTA is determined. In order to get a more effective ripple reduction, a low pass filter (LPF) is employed which also located on the outside of GBW, shown in Fig. 4. The enhanced suppression is described in Fig. 3(c).

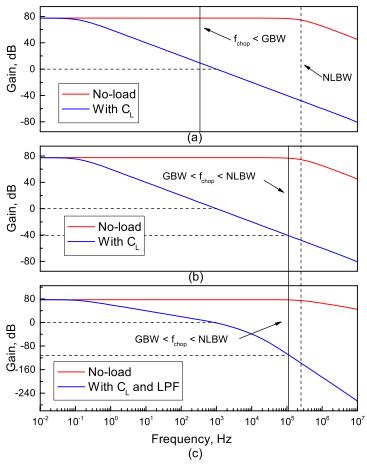


Fig. 3. AC response of OTA, a) Conventional chopping, b) Proposed COOB, c) Proposed COOB with LPF.





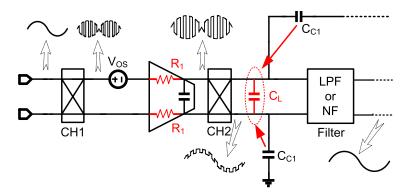


Fig. 4. COOB structure with filter.

Assuming that P_0 is the dominant pole which determined by OTA and C_{C1} , P_1 , P_2 , P_3 are introduced by the LPF, and A_{v1} is the DC gain of the OTA. Ignoring the interaction of poles, the ripple suppression could be described by $\frac{V_{ripple}}{V_{offset}}$:

$$\frac{V_{ripple}}{V_{offset}} = -80 \lg(f_{chop}) + 20 \lg(P_0 \cdot P_1 \cdot P_2 \cdot P_3) + A_{v1} - 3. \tag{2}$$

With the help of LPF, the ripple will get an extra $-60 \, \mathrm{dB/dec}$ roll-off reduction. A notch filter (NF) located on the outside of GBW could also meet the needs as well as LPF, which can select some specific frequency out.

3 Instrumentation amplifier with COOB

The proposed COOB has been employed in instrumentation amplifiers shown in Fig. 5. These amplifiers consist of 2 signal paths, a low frequency path and a high frequency path. The low frequency path provides the main DC gain, and chopping is used for cancelling the offset and 1/f noise with COOB to reduce the ripple. The high frequency path makes the overall amplifier stable and generates a suitable

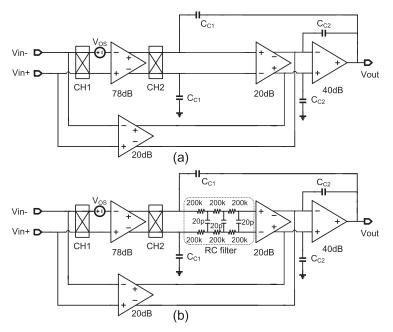


Fig. 5. Instrumentation amplifiers with chopping, a) Conventional instrumentation amplifier, b) COOB instrumentation amplifier with LPF.





phase margin [4]. The GBW of the chopping stage (GBW_1) and GBW of the overall circuit are determined by C_{C1} and C_{C2} , respectively. The ripple will be suppressed by COOB structure and then amplified by $A_{\nu 2}$ and $A_{\nu 3}$. However, $A_{\nu 2} \cdot A_{\nu 3}$ is smaller than $\frac{V_{ripple}}{V_{offset}}$ so that the ripple in COOB instrumentation amplifier is still been suppressed.

The LPF is designed to a RC filter which provides P_1 , P_2 and P_3 . Because the dominant pole P_0 is determined by OTA and C_{C1} , the parameters of the RC filter are acceptable in IC. On the other hand, because of the high frequency path, the stability of the overall circuits will not be influenced by the filter, so that the precision and matching of the RC filter are not concerned. Moreover, the RC filter consumes no additional power.

4 Simulation results

Two instrumentation amplifiers AMP1 and AMP2 were designed shown in Fig. 5(a) and (b), and the amplifiers were simulated on UMC 0.18 um CMOS process with the help of spetreRF. In order to observe the ripple reduction, these amplifiers were used as 20 dB gain closed-loop amplifiers, and the V_{OS} was set up to 10 mV. After the transient analysis, the output signal were calculated by Power Spectral Density (PSD) function. The transient output signal and their PSD are shown in Fig. 6. We have obtained 3 groups of data: AMP1 with $f_{chop} = 1k(< GBW_1)$, AMP1 with $f_{chop} = 200k(> GBW_1)$, AMP2 with $f_{chop} = 200k(> GBW_1)$.

According to the transient simulation results, the ripple has been cancelled obviously, from peak-to-peak voltage $216mV_{p-p}$ (In Band), $114.4mV_{p-p}$ (Out Of Band), to $18.2\mu V_{p-p}$ (Out Of Band with LPF). Compared with chopping in band, the PSD results showed that the ripple has been reduced by $-17.5\,\mathrm{dB}$ and

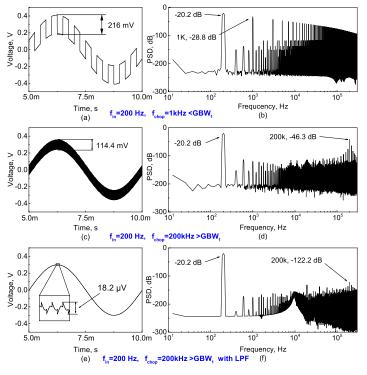


Fig. 6. Ripple in transient simulation and its PSD results, $V_{OS} = 10 \,\text{mV}$.





-93.4 dB. The simulation results of key parameters of the two amplifiers are listed in Table I.

Table I. Performance summary of AMP1 and AMP2

Parameter	AMP1		AMP2
Voltage supply (V)	1.8		1.8
C_L (pF)	20		20
Power (µA)	148		148
DC Gain (dB)	131.4		131.4
GBW_1 (kHz)	1.19		1.19
Overall GBW (kHz)	1200		1210
PM (deg)	73		73
Chopping Mode	Chopping In Band	COOB	COOB
f_{chop} (kHz)	1	200	200
Gain with chopping (dB)	130.9	128.5	128.5
V_{ripple}	$216mV_{p-p}$	$114.4mV_{p-p}$	18.2 uV _{p-p}

5 Conclusion

A method for cancelling the ripple in chopper amplifiers named COOB is proposed in this paper. With the COOB technique, two instrumentation amplifiers were designed as examples. Simulation results showed that the ripple can be reduced by $-93.4\,\mathrm{dB}$, from $216mV_{p-p}$ to $18.2\mu V_{p-p}$ without additional power consumption.

Acknowledgments

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