[DOI: 10.2197/ipsjtsldm.15.1]

Message from the Editor-in-Chief

Atsushi Takahashi^{1,a)}

On behalf of the editorial board, I am pleased to present the fifteenth volume of the IPSJ Transactions on System LSI Design Methodologies (TSLDM). TSLDM aims to provide the best opportunity for all researchers in the field of the design, the electronic design automation, and the methodologies of system LSIs or systems-on-a-chip to publish and learn about state-of-the-art works. TSLDM covers various aspects of system LSIs or systems-on-a-chip design methodology, including design experience, architecture, synthesis, modeling, simulation, verification, testing, embedded software, emerging applications, and so on.

The fifteenth volume consists of the June issue. This volume contains peer-reviewed papers. Our peer-reviewed papers are classified into three types: Regular, Short and Survey. A regular paper is a description of research that is academic or technical in nature or products of development which is valuable to members of the IPSJ from the viewpoints of novelty and usefulness. A short paper is a brief description of research that is academic or technical in nature or products of development which is valuable to members of the IPSJ from the viewpoints of novelty and usefulness. A survey paper is a description of technical trends or perspectives that are organized, classified and/or evaluated from author's own viewpoint which contains useful information for members of the IPSJ.

As an open-access online journal, TSLDM allows authors to disseminate their excellent works to our research community easily. Every paper can be accessed anytime anywhere. Every issue is published according to the predetermined schedule which is announced in call-for-papers. Authors can plan to publish their excellent works in a timely manner.

TSLDM was launched in 2008, since then TLSDM has become one of the most prestigious journals in our research area. We are still facing difficulty under the circumstances of covid-19 to prevent us from discussing at face-to-face meetings and etc. Editorial committee meetings are also held fully online. Research works might be stagnated temporally under the rapid paradigm and environmental changes but we have to stimulate our society by accelerating our researches and by distributing excellent works.

Finally, I would like to thank all the authors who submitted their excellent research works to TSLDM that form the basis of our technical excellence.

Associate Editors-in-Chief:

Ittetsu Taniguchi (Osaka University) Shinobu Nagayama (Hiroshima City University) Hiroki Hosoda (Sony LSI Design)

Associate Editors:

Motoki Amagasaki (Kumamoto University) Ryuji Hada (Toshiba) Shiho Hagiwara (Fujitsu Laboratories) Masayuki Hiromoto (Fujitsu Laboratories) Takashi Imagawa (Meiji University) Satoshi Komatsu (Tokyo Denki University) Yutaka Masuda (Nagoya University) Lin Meng (Ritsumeikan University) Yukio Mitsuyama (Kochi University of Technology) Shinichi Nishizawa (Waseda University) Satoshi Ohtake (Oita University) Takayuki Onishi (NTT) Sombatsiri Salita (Siam Commercial Bank) Yuichi Sakurai (Hitachi) Kazuyoshi Takagi (Mie University) Yoshinori Takeuchi (Kindai University) Nozomu Togawa (Waseda University)

¹ Tokyo Institute of Technology, Meguro, Tokyo 152–8550, Japan

a) atsushi@ict.e.titech.ac.jp