

submissions.

## Message from the Editor-in-Chief

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This is the second issue of IPSJ Transactions on System LSI Design Methodology (TSLDM). TSLDM aims to provide the best opportunity for all researchers working on Design, Electronic Design Automation and Design Methodologies of a System LSI (System-on-Chip) to publish and learn state-of-the-art achievements in the various fields.

TSLDM is an open-access online journal so that authors can efficiently disseminate their important accomplishments to the widest R&D community of the world. Papers can be browsed and downloaded worldwide through internet access.

Another important feature of TSLDM is a timely and rapid publication. Every issue has a predetermined editorial schedule announced in the call for papers. Authors can therefore expect the date of publication to be just nine months after the submission is due. This makes it more attractive to submit and publish up-to-date information on research and development. We hope that TSLDM offers an ideal forum for the exchange of ideas about the challenges and solutions for the future.

This issue consists of two invited papers on system-level formal verification and low-power circuit design, and nine regular papers on a wide variety of important topics in System LSI Design Methodology, which clearly indicates that TSLDM provides comprehensive coverage of the modeling, analysis, synthesis, verification and design of System LSIs.

TSLDM is currently on a biannual schedule. The next issue is planned to be published in August 2009. By the time the current issue is published, the fourth issue will be ready for accepting submissions. We look forward to receiving your

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