## Message from the Editor-in-Chief

## Hidetoshi Onodera<sup>†1</sup>

On behalf of the editorial board, I am pleased to present the third volume of IPSJ Transactions on System LSI Design Methodology (TSLDM). TSLDM aims to provide the best opportunity for all researchers working on Design, Electronic Design Automation and Design Methodologies of a System LSI (System-on-Chip) to publish and learn state-of-the-art achievements in various fields.

The first issue of TSLDM was successfully launched in August 2008. In 2009 we have delivered February and August issues which have been archived in the second volume of TSLDM. This year, similar to the previous year, the third volume is going to contain February and August issues.

There are two distinctive features that differentiate TSLDM from other transactions and journals. TSLDM is unique in providing an open-access online publication so that authors can efficiently disseminate their important accomplishments to the widest R&D community of the world. Papers can be browsed and downloaded worldwide through internet access without any restrictions. Another important feature of TSLDM is a timely and rapid publication. Every issue has a predetermined editorial schedule announced in the call for papers. Authors can therefore expect the date of publication to be just nine months after the submission is due. This makes it more attractive to submit and publish up-to-date information on research and development.

TSLDM is currently on a biannual schedule. We hope that TSLDM offers an ideal forum for the exchange of ideas about the challenges and solutions for the future. Latest call for papers and instructions for paper submission can be found on our homepage. We look forward to receiving your contributions.

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