

Message from the Editor-in-Chief

NOZOMU TOGAWA^{1,a)}

On behalf of the editorial board, I am pleased to present the ninth volume of the IPSJ Transactions on System LSI Design Methodologies (TSLDM). TSLDM aims to provide the best opportunity for all researchers working on the design, the electronic design automation, and the methodologies of system LSIs or systems-on-a-chip to publish and learn about recent, state-of-the-art works.

Similar to previous volumes, the ninth volume contains the February and August issues. Each issue, in turn, contains invited papers written by world-renowned researchers and many high-quality, peer-reviewed papers.

Established in 2008, TSLDM has become one of the most prestigious journals in our research area. I would like to express my sincere gratitude to Prof. Hidetoshi Onodera, the 1st Editor-in-Chief of the journal, and Prof. Hiroyuki Tomiyama, the 2nd Editor-in-Chief of the journal. As the 3rd Editor-in-Chief, I would like to take this opportunity to further enhance this prestigious journal. I would also like to thank all the past committee members, authors, and reviewers. Without their great contributions, TSLDM would have never acquired such a prestigious status in such a very short period. I will also do my best, together with the Editors and Associate Editors.

TSLDM has very unique features which distinguish it from other journals in our research community. As an open-access and online publication, TSLDM allows authors to disseminate their important achievements to the entire research community, wherever they may be in the world. Every paper can be downloaded from anywhere through the Internet. Another unique feature is that it is a timely and rapid publication. Every issue has a predetermined editorial schedule which is announced in the respective call-for-papers. Authors can plan to publish their recent and important works in advance.

Finally, but not least, I would like to thank all the authors who submitted their research works since their contributions form the basis of our technical excellence. Without a doubt, TSLDM will continue to be an ideal journal because of its open-access policy, timely publication, and established reputation.

Associate Editors-in-Chief:

Masanori Hashimoto (Osaka University)
Kazuyoshi Takagi (Kyoto University)
Yasuhiro Takashima (University of Kitakyushu)

Associate Editors:

Hideho Arakida (Toshiba)
Yoshinobu Higami (Ehime University)
Akio Hirata (Panasonic Semiconductor Solutions)
Tetsuo Hironaka (Hiroshima City University)
Eiichi Hosoya (NTT)
Tetsuya Iizuka (The University of Tokyo)
Tohru Ishihara (Kyoto University)
Toshiki Kanamoto (Renesas Electronics)
Mineo Kaneko (Japan Advanced Institute of Science and Technology)
Keiji Kimura (Waseda University)
Takeshi Kumaki (Ritsumeikan University)
Takeshi Matsumoto (Ishikawa National College of Technology)
Kenshu Seto (Tokyo City University)
Kotaro Shimamura (Hitachi)
Makoto Sugihara (Kyushu University)
Noriaki Suzuki (NEC)
Atsushi Takahashi (Tokyo Institute of Technology)
Hiroyuki Tomiyama (Ritsumeikan University)
Masayuki Yamaguchi (Sharp)
Hiroaki Yoshida (Fujitsu Laboratories of America)

¹ Waseda University, Shinjuku, Tokyo 169-8555, Japan

^{a)} ntogawa@waseda.jp