Lecture Notes in Computer Science 3707

Commenced Publication in 1973
Founding and Former Series Editors:
Gerhard Goos, Juris Hartmanis, and Jan van Leeuwen

Doron A. Peled   Yih-Kuen Tsay (Eds.)

Automated Technology for Verification and Analysis

Third International Symposium, ATVA 2005
Taipei, Taiwan, October 4-7, 2005
Proceedings

Springer

Volume Editors

Doron A. Peled
University of Warwick
Department of Computer Science
Coventry, CV4 7AL, UK
E-mail: doron@dcs.warwick.ac.uk

Yih-Kuen Tsay
National Taiwan University
Department of Information Management
No. 1, Sec. 4, Roosevelt Rd., Taipei 106, Taiwan (ROC)
E-mail: tsay@im.ntu.edu.tw

Preface

The Automated Technology for Verification and Analysis (ATVA) international symposium series was initiated in 2003, responding to a growing interest in formal verification spurred by the booming IT industry, particularly hardware design and manufacturing in East Asia. Its purpose is to promote research on automated verification and analysis in the region by providing a forum for interaction between the regional and the international research/industrial communities of the field. ATVA 2005, the third of the ATVA series, was held in Taipei, Taiwan, October 4–7, 2005. The main theme of the symposium encompasses design, complexities, tools, and applications of automated methods for verification and analysis. The symposium was co-located and had a two-day overlap with FORTE 2005, which was held October 2–5, 2005.

We received a total of 95 submissions from 17 countries. Each submission was assigned to three Program Committee members, who were helped by their subreviewers, for rigorous and fair evaluation. The final deliberation by the Program Committee was conducted over email for a duration of about 10 days after nearly all review reports had been collected. In the end, 33 papers were selected for inclusion in the program. ATVA 2005 had three keynote speeches given respectively by Amir Pnueli (joint with FORTE 2005), Zohar Manna, and Wolfgang Thomas. The main symposium was preceded by a tutorial day, consisting of three two-hour lectures given also by the keynote speakers.

ATVA 2005 was supported by National Science Council, Ministry of Education, and Academia Sinica of Taiwan and also by the Center for Information and Electronics Technologies at National Taiwan University and Cadence Design Systems. Their generous sponsorships are gratefully acknowledged. We would like to thank the Program Committee members and their subreviewers for the hard work in evaluating the submissions and selecting the program. We thank the keynote speakers for their extra effort in delivering the tutorials. We thank the Steering Committee for their advice, particularly Farn Wang, who also served as program chair of the two previous ATVA symposia and of FORTE 2005, for providing many valuable suggestions and for being very cooperative with the joint events of ATVA 2005 and FORTE 2005.

For administrative support, we thank the Department of Information Management and the Department of Electrical Engineering at National Taiwan University. In particular, we thank Mr. Yu-Fang Chen for maintaining the conference Web site among many other administrative chores. We thank also the MyReview team for making available a free and convenient submission system.

October 2005   Doron A. Peled and Yih-Kuen Tsay

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