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Field-Programmable Gate Arrays:

Architectures and Tools for Rapid Prototyping

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on Field-Programmable Logic and Applications
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Preface

This book contains papers first presented at the 2nd International Workshop on Field-Programmable Logic and Applications (FPL'92), held in Vienna, Austria, from August 31 to September 2, 1992.

The growing importance of field-programmable devices, especially of field-programmable gate arrays, was demonstrated by the increased number of papers submitted in 1992. For the workshop in Vienna 70 papers were submitted. It was pleasing to see the high quality of these papers and their international character with contributions from more than 20 countries. The following list shows the distribution of origins of the papers submitted to FPL'92 (some papers were written by an international team):

Australia:	1	Austria:	4
Canada:	1	Czechoslowakia:	1
Finland:	3	France:	2
Germany:	16	Italy:	3
Japan:	3	Norway:	3
Russia:	1	Spain:	4
Sweden:	4	Switzerland:	1
United Kingdom:	4	USA (incl. Hawaii):	21

From the 70 submitted papers, 23 were selected for this book. The first three papers discuss strategic issues and give surveys. Three papers deal with new FPGA architectures and five papers introduce methods for tools. The last twelve papers report applications focusing on rapid prototyping or new FPGA-based computer architectures.

We would like to thank the members of the technical program committee for reviewing the papers submitted to the workshop. Our thanks go also to the authors who wrote the extended papers for this issue and the reviewers for their timely work on all manuscripts. Especially we would like to thank Helmut Reinig for managing the reviewing process and handling the manuscripts and program planning.

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