Lecture Notes in Computer Science

13152

Founding Editors

Gerhard Goos

Karlsruhe Institute of Technology, Karlsruhe, Germany

Juris Hartmanis

Cornell University, Ithaca, NY, USA

Editorial Board Members

Elisa Bertino

Purdue University, West Lafayette, IN, USA

Wen Gao

Peking University, Beijing, China

Bernhard Steffen

TU Dortmund University, Dortmund, Germany

Gerhard Woeginger

RWTH Aachen, Aachen, Germany

Moti Yung

Columbia University, New York, NY, USA

More information about this subseries at https://link.springer.com/bookseries/7407

Christophe Cérin · Depei Qian · Jean-Luc Gaudiot · Guangming Tan · Stéphane Zuckerman (Eds.)

Network and Parallel Computing

18th IFIP WG 10.3 International Conference, NPC 2021 Paris, France, November 3–5, 2021 Proceedings



Editors Christophe Cérin Université Sorbonne Paris Nord, LIPN Villetaneuse. France

Jean-Luc Gaudiot University of California at Irvine Irvine, CA, USA

Stéphane Zuckerman ETIS Laboratory CY Cergy Paris Université, ENSEA, CNRS Cergy, France Depei Qian Beihang University Beijing, China

Guangming Tan Institute of Computing Technology Beijing, China

ISSN 0302-9743 ISSN 1611-3349 (electronic) Lecture Notes in Computer Science ISBN 978-3-030-93570-2 ISBN 978-3-030-93571-9 (eBook) https://doi.org/10.1007/978-3-030-93571-9

LNCS Sublibrary: SL1 - Theoretical Computer Science and General Issues

© IFIP International Federation for Information Processing 2022

This work is subject to copyright. All rights are reserved by the Publisher, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, reuse of illustrations, recitation, broadcasting, reproduction on microfilms or in any other physical way, and transmission or information storage and retrieval, electronic adaptation, computer software, or by similar or dissimilar methodology now known or hereafter developed.

The use of general descriptive names, registered names, trademarks, service marks, etc. in this publication does not imply, even in the absence of a specific statement, that such names are exempt from the relevant protective laws and regulations and therefore free for general use.

The publisher, the authors and the editors are safe to assume that the advice and information in this book are believed to be true and accurate at the date of publication. Neither the publisher nor the authors or the editors give a warranty, expressed or implied, with respect to the material contained herein or for any errors or omissions that may have been made. The publisher remains neutral with regard to jurisdictional claims in published maps and institutional affiliations.

This Springer imprint is published by the registered company Springer Nature Switzerland AG The registered company address is: Gewerbestrasse 11, 6330 Cham, Switzerland

Preface

Welcome to the proceedings of the eighteenth edition of the International Conference on Network and Parallel Computing (NPC 2021), held in Paris, France, during November 3–5, 2021.

First, we would like to honor the memory of Guang R. Gao, who, on top of having spent his life making substantial contributions to the field of high-performance computing in general, was a driving force behind NPC. Along with a few other people from the NPC Steering Committee, he suggested to hold NPC outside of Asia to broaden its audience, and have it occur in a European venue.

Second, we would like to thank all the committee members, from the program vice-chairs to the local organizing chairs and the reviewers. We are thinking in particular of Aziza Lounis from DNAC, Abdulhalim Dandoush from ESME Sudria, Éric Renault from ESIEE Paris, and Khaled Boussetta from Université Sorbonne Paris Nord. A special thank you to En Shao from the Institute of Computing Technology (ICT), China. And of course, we should not forget the extremely constructive influence of Kemal Ebcioglu.

A word of gratitude to the staff at the IEEE Computer Society and at IFIP for their indefatigable work. They immensely contributed to improving the scientific impact of NPC. We thank all the people we interacted with to improve the scientific impact of NPC. They will recognize themselves.

This conference was organized in cooperation with two IEEE Technical Committees (TCS) and two IEEE Special Technical Communities (STC):

- -Technical Committee on Parallel Processing TCPP, Anne Benoit, France.
- -Technical Committee on Cloud Computing TCCLD, Robert Hsu, Taiwan.
- -Special Technical Community on Multicore STC Multicore, Lawrence Rauchwerger, USA.
- -Special Technical Community on Dataflow STC Dataflow, Guang R. Gao, USA and Stéphane Zuckerman, France.

Finally, we would like to thank our home institutions for their support: CY Cergy Paris Université, Université Sorbonne Paris Nord (USPN) including the MathSTIC research group, ESIEE, ESME Sudria, and Université Gustave Eiffel. Thank you very much, we greatly appreciated their help and assistance.

A total of 62 submissions were received in response to our call for papers. These papers originated from Asia (China and Japan), Africa, Europe, and North America (USA). Each submission was sent to at least three reviewers, with an average of four reviewers per submission, and up to six reviewers. Each paper was judged according to its originality, innovation, readability, and relevance to the expected audience. Based on the reviews received, 20 full papers (about 32%) were selected to be published as LNCS proceedings. Among these, six papers were further selected to be extended and submitted to a Special Issue in the International Journal of Parallel Programming. Four main themes were identified during the conference: Networks and Communications; Storage; System

vi Preface

Software; and Applications and Algorithms. Two "Best Papers" sessions were also held, the first of which was oriented toward storage issues, while the other one was geared toward applications and algorithms.

We would also like to recognize our three guest speakers who gave exciting presentations: Yutang Lu, Sun Yat-sen University, China, who demonstrated current and future challenges to build and program supercomputers; Avi Mendelson, Technion, Israel, who spoke about data-centric computations for future high-performance systems; and Anne Benoit, ENS Lyon, France, who tackled the issue of resilience and fault-tolerance in current and future supercomputing systems.

Enjoy NPC 2021!

November 2021

Christophe Cérin Depei Qian Jean-Luc Gaudiot Guangming Tan Stéphane Zuckerman

Organization

NPC 2021 was jointly organized by ESME Sudria, ESIEE Paris, Université Gustave Eiffel, Laboratoire d'Informatique Gaspard Monge (LIGM), CY Cergy Paris Université, Université Sorbonne Paris Nord, and DNAC.

General Co-chairs

Christophe Cérin Université Sorbonne Paris Nord, France

Depei Qian Beihang University, China

Program Co-chairs

Jean-Luc Gaudiot University of California, Irvine, USA Stéphane Zuckerman CY Cergy Paris Université, France

Guangming Tan ICT, China

Local Arrangements Chair

Abdulhalim Dandoush ESME Sudria, France

Publicity Co-chairs

Khaled Boussetta Université Sorbonne Paris Nord, France

Chen Liu Clarkson, USA En Shao ICT, China

Publication Chair

Éric Renault ESIEE Paris, France

Web Chair

Elia Kallas DNAC, France

Advisory Committee

Hai Jin (Chair) Huazhong University of Science and Technology,

China

Wenguang Chen Tsinghua University, China

Organization

viii

Yunguan Zhang ICT, China

Weisong Shi Wayne State University, USA

Shengzhong Feng National Supercomputing Center in Shenzhen,

China

Victor Prasanna University of Southern California, USA

Steering Committee

Kemal Ebcioglu (Chair) Global Supercomputing, USA

Hai Jin (Vice Chair) Huazhong University of Science and Technology,

China

Chen Ding University of Rochester, USA
Jack Dongarra University of Tennessee, USA
Guangrong Gao University of Delaware, USA

Jean-Luc Gaudiot University of California, Irvine, USA

Tony Hey Science and Technology Facilities Council, UK

Guojie Li ICT, China

Yoichi Muraoka Waseda University, Japan

Viktor Prasanna University of Southern California, USA

Daniel Reed University of Utah, USA Weisong Shi Wayne State University, USA

Ninghui Sun ICT, China Zhiwei Xu ICT, China

Technical Program Committee

Stéphane Zuckerman

CY Cergy Paris Université, France

Christophe Cérin

Université Sorbonne Paris Nord, France

Anna Kobusinska

Poznan University of Technology, Poland

Dezun Dong

National University of Defense Technology,

China

Avi Mendelson Technion, Israel
En Shao ICT, China
Bo Yu PerceptIn, China

Jean-Luc Gaudiot University of California, Irvine, USA Quan Chen Shanghai Jiao Tong University, China

Éric Renault ESIEE Paris, France

Philippe Clauss Inria, ICube, University of Strasbourg, France
Bruno Raffin Inria and University of Grenoble, France
Theo Ungerer University of Augsburg, Germany

Weile Jia ICT, China

Keqiu Li Dalian University of Technology, China

William Jalby Université de

Versailles-Saint-Ouentin-en-Yvelines, France

Weifeng Liu China University of Petroleum, China

Hai Jin Huazhong University of Science and Technology,

China

Won Woo Ro

Claude Tadonki

Yonsei University, South Korea

Mines ParisTech, France

Jean-Thomas Acquaviva DDN, France

Roberto Giorgi University of Siena, Italy

Gabriel Paillard Federal University of Ceará, Brazil

William Chu Tunghai University, Taiwan

Shaoshan Liu PerceptIn, China

Piyush Sao Georgia Institute of Technology, USA Heithem Abbes University of Tunis El Manar, Tunisia

Mostapha Zbakh ENSIAS, Mohammed V University of Rabat,

Morocco

Keiji Kimura Waseda University, Japan

R. Govindarajan Indian Institute of Science, India

Albert Cohen Inria, France

Sven Groppe University of Lubeck, Germany

Felix Freitag Universitat Politècnica de Catalunya, Spain

Marc Perache CEA DAM, France

Arnaud Lallouet Huawei Technologies Ltd, France

Cezary Mazurek Poznan Supercomputing and Networking Center,

Poland

Arthur Stoutchinin ST Microelectronics, France
Pierre Manneback University of Mons, Belgium

Patrice Darmon Umanis R&D, France

Yiannis Papadopoulos AMD, USA

Alba Cristina M. A. Melo University of Brasilia, Brazil Roberto Hsu Asia University, Taiwan

Sponsoring Institutions

FR3734 MathSTIC USPN/CNRS

Contents

Algorithms and Applications	
High Resolution of City-Level Climate Simulation by GPU with Multi-physical Phenomena	3
dgQuEST: Accelerating Large Scale Quantum Circuit Simulation through Hybrid CPU-GPU Memory Hierarchies Tianyu Feng, Siyan Chen, Xin You, Shuzhang Zhong, Hailong Yang, Zhongzhi Luan, and Depei Qian	16
vSketchDLC: A Sketch on Distributed Deep Learning Communication via Fine-grained Tracing Visualization Yanghai Wang, Shuo Ouyang, Dezun Dong, Enda Yu, and Xiangke Liao	28
Scalable Algorithms Using Sparse Storage for Parallel Spectral Clustering on GPU Guanlin He, Stephane Vialle, Nicolas Sylvestre, and Marc Baboulin	40
XSP: Fast SSSP Based on Communication-Computation Collaboration Xinbiao Gan, Wen Tan, Menghan Jia, Jie Liu, and Yiming Zhang	53
A Class of Fast and Accurate Multi-layer Block Summation and Dot Product Algorithms Kang He, Roberto Barrio, Lin Chen, Hao Jiang, Jie Liu, Tongxiang Gu, and Jin Qi	64
A KNN Query Method for Autonomous Driving Sensor Data Tang Jie, Zhang Jiehui, Zeng Zhixin, and Liu Shaoshan	76
System Software and Resource Management	
A Novel Task-Allocation Framework Based on Decision-Tree Classification Algorithm in MEC Wenwen Liu, Zhaoyang Yu, Meng Yan, Gang Wang, and Xiaoguang Liu	93
QoS-Aware Scheduling for Cellular Networks Using Deep Reinforcement Learning	105
Jonathan Robert Malin, Gun Ko, and Won Woo Ro	102

Adaptive Buffering Scheme for PCM/DRAM-Based Hybrid Memory	
Architecture Xiaoliang Wang, Kaimeng Chen, and Peiquan Jin	118
Efficiency-First Fault-Tolerant Replica Scheduling Strategy for Reliability Constrained Cloud Application	131
Yingxue Zhang, Guisheng Fan, Huiqun Yu, and Xingpeng Chen	
Towards an Optimized Containerization of HPC Job Schedulers Based on Namespaces	144
Tarek Menouer, Nicolas Greneche, Christophe Cérin, and Patrice Darmon	
Architecture of an On-Time Data Transfer Framework in Cooperation with Scheduler System	157
Storage	
Data Delta Based Hybrid Writes for Erasure-Coded Storage Systems	171
BDCuckoo: an Efficient Cuckoo Hash for Block Device Xianqi Zheng, Jia Ma, Yubo Liu, and Zhiguang Chen	183
A Two Tier Hybrid Metadata Management Mechanism for NVM Storage	
System	195
A Novel CFLRU-Based Cache Management Approach for NAND-Based SSDs Haodong Lin, Jun Li, Zhibing Sha, Zhigang Cai, Jianwei Liao, and Yuanquan Shi	214
Networks and Communications	
Taming Congestion and Latency in Low-Diameter High-Performance Datacenters Renjie Zhou, Dezun Dong, Shan Huang, Zejia Zhou, and Yang Bai	229
Evaluation of Topology-Aware All-Reduce Algorithm for Dragonfly	243
Networks Junchao Ma, Dezun Dong, Cunlu Li, Ke Wu, and Liquan Xiao	243

	Contents	xiii
MPICC: Multi-Path INT-Based Congestion Control in Datacenter Networks		256
Guoyuan Yuan, Dezun Dong, Xingyun Qi, and Baokang Zhao		
Author Index		269