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Intelligent Systems Design and Applications

17th International Conference on Intelligent Systems Design and Applications (ISDA 2017) Held in Delhi, India, December 14–16, 2017



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Preface

Welcome to the Proceedings of the 17th International Conference on Intelligent Systems Design and Applications (ISDA17), which was held in South Asian University, Delhi, India, during December 14–16, 2017. ISDA 2017 is jointly organized by the Machine Intelligence Research Labs (MIR Labs), USA, and South Asian University, Delhi, India.

ISDA 2017 brings together researchers, engineers, developers, and practitioners from academia and industry working in all interdisciplinary areas of intelligent systems and system engineering to share their experiences, and to exchange and cross-fertilize their ideas. The aim of ISDA 2017 is to serve as a forum for the dissemination of state-of-the-art research and development of intelligent systems, intelligent technologies, and applications. ISDA 2017 was organized in conjunction with the 7th World Congress on Information and Communication Technologies (WICT 2017).

The themes of the contributions and scientific sessions range from theories to applications, reflecting a wide spectrum of the coverage of intelligent systems and computational intelligence areas. ISDA 2017 received submissions from over 30 countries, and each paper was reviewed by at least 5 reviewers in a standard peer-review process. Based on the recommendation by 5 independent referees, finally about 100 papers were accepted for publication in the proceedings published by Springer, Verlag.

Many people have collaborated and worked hard to produce the successful ISDA 2017 conference. First, we would like to thank all the authors for submitting their papers to the conference, for their presentations and discussions during the conference. Our thanks go to Program Committee members and reviewers, who carried out the most difficult work by carefully evaluating the submitted papers. Our special thanks to Patricia Melin, Tijuana Institute of Technology, Tijuana, Mexico, and Alexander Gelbukh, Instituto Politécnico Nacional, Mexico City, Mexico, for the exciting plenary talks.

vi Preface

We express our sincere thanks to special session chairs and organizing committee chairs for helping us to formulate a rich technical program.

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