Lecture Notes in Computer Science

11128

Commenced Publication in 1973
Founding and Former Series Editors:
Gerhard Goos, Juris Hartmanis, and Jan van Leeuwen

Editorial Board

David Hutchison

Lancaster University, Lancaster, UK

Takeo Kanade

Carnegie Mellon University, Pittsburgh, PA, USA

Josef Kittler

University of Surrey, Guildford, UK

Jon M. Kleinberg

Cornell University, Ithaca, NY, USA

Friedemann Mattern

ETH Zurich, Zurich, Switzerland

John C. Mitchell

Stanford University, Stanford, CA, USA

Moni Naor

Weizmann Institute of Science, Rehovot, Israel

C. Pandu Rangan

Indian Institute of Technology Madras, Chennai, India

Bernhard Steffen

TU Dortmund University, Dortmund, Germany

Demetri Terzopoulos

University of California, Los Angeles, CA, USA

Doug Tygar

University of California, Berkeley, CA, USA

Gerhard Weikum

Max Planck Institute for Informatics, Saarbrücken, Germany

More information about this series at http://www.springer.com/series/7408

Bronis R. de Supinski · Pedro Valero-Lara Xavier Martorell · Sergi Mateo Bellido Jesus Labarta (Eds.)

Evolving OpenMP for Evolving Architectures

14th International Workshop on OpenMP, IWOMP 2018 Barcelona, Spain, September 26–28, 2018 Proceedings



Editors
Bronis R. de Supinski
Lawrence Livermore National Laboratory
Livermore, CA
USA

Pedro Valero-Lara

Barcelona Supercomputing Center Barcelona, Barcelona Spain

Xavier Martorell Universitat Politècnica de Catalunya Barcelona Spain Sergi Mateo Bellido Barcelona Supercomputing Center Barcelona, Barcelona Spain

Jesus Labarta Universitat Politècnica de Catalunya Barcelona, Barcelona Spain

ISSN 0302-9743 ISSN 1611-3349 (electronic) Lecture Notes in Computer Science ISBN 978-3-319-98520-6 ISBN 978-3-319-98521-3 (eBook) https://doi.org/10.1007/978-3-319-98521-3

Library of Congress Control Number: 2018950652

LNCS Sublibrary: SL2 – Programming and Software Engineering

© Springer Nature Switzerland AG 2018

This work is subject to copyright. All rights are reserved by the Publisher, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, reuse of illustrations, recitation, broadcasting, reproduction on microfilms or in any other physical way, and transmission or information storage and retrieval, electronic adaptation, computer software, or by similar or dissimilar methodology now known or hereafter developed.

The use of general descriptive names, registered names, trademarks, service marks, etc. in this publication does not imply, even in the absence of a specific statement, that such names are exempt from the relevant protective laws and regulations and therefore free for general use.

The publisher, the authors and the editors are safe to assume that the advice and information in this book are believed to be true and accurate at the date of publication. Neither the publisher nor the authors or the editors give a warranty, express or implied, with respect to the material contained herein or for any errors or omissions that may have been made. The publisher remains neutral with regard to jurisdictional claims in published maps and institutional affiliations.

This Springer imprint is published by the registered company Springer Nature Switzerland AG The registered company address is: Gewerbestrasse 11, 6330 Cham, Switzerland

Preface

OpenMP is a widely accepted, standard application programming interface (API) for high-level shared-memory parallel programming in Fortran, C, and C++. Since its introduction in 1997, OpenMP has gained support from most high-performance compiler and hardware vendors. Under the direction of the OpenMP Architecture Review Board (ARB), the OpenMP specification has evolved up to and beyond version 4.5. The 4.5 version includes several refinements to existing support for heterogeneous hardware environments, many enhancements to its tasking model including the task-loop construct, and support for doacross loops. As indicated in TR7, OpenMP 5.0, will include significant new features, such as mechanisms for memory affinity and the standardization of tool APIs, and improvements in existing ones, such as the device and tasking constructs.

The evolution of the standard would be impossible without active research in OpenMP compilers, runtime systems, tools, and environments. OpenMP is important both as a standalone parallel programming model and as part of a hybrid programming model for massively parallel, distributed memory systems built from multicore, manycore, and heterogeneous node architectures. Overall, OpenMP offers important features that can improve the scalability of applications on expected exascale architectures.

The community of OpenMP researchers and developers is united under the cOM-Punity organization. This organization has held workshops on OpenMP around the world since 1999: the European Workshop on OpenMP (EWOMP), the North American Workshop on OpenMP Applications and Tools (WOM-PAT), and the Asian Workshop on OpenMP Experiences and Implementation (WOMPEI), which attracted annual audiences from academia and industry. The International Workshop on OpenMP (IWOMP) consolidated these three workshop series into a single annual international event that rotates across Europe, Asia-Pacific, and the Americas. The first IWOMP workshop was organized under the auspices of cOMPunity. Since that workshop, the IWOMP Steering Committee has organized these events and guided the development of the series. The first IWOMP meeting was held in 2005, in Eugene, Oregon, USA. Since then, meetings have been held each year, in: Reims, France; Beijing, China; West Lafayette, USA; Dresden, Germany; Tsukuba, Japan; Chicago, USA; Rome, Italy; Canberra, Australia; Salvador, Brazil; Aachen, Germany; Nara, Japan; and Stony Brook, USA. Each workshop has drawn participants from research and industry throughout the world. IWOMP 2018 continued the series with technical papers and tutorials. The IWOMP meetings have been successful in large part due to generous support from numerous sponsors.

The IWOMP website (www.iwomp.org) provides information on the latest event, as well as links to websites from previous years' events. This book contains the proceedings of IWOMP 2018. The workshop program included 16 technical papers, two keynote talks, and a tutorial on OpenMP. The paper "The Impact of Taskyield on the

VI Preface

Design of Tasks Communicating Through MPI" by Joseph Schuchart, Keisuke Tsugane, Jose Gracia, and Mitsuhisa Sato was selected for the Best Paper Award. All technical papers were peer reviewed by at least three different members of the Program Committee.

September 2018

Bronis R. de Supinski Sergi Mateo Bellido

Organization

Program Committee Co-chairs

Bronis R. de Supinski Lawrence Livermore National Laboratory, USA Sergi Mateo Bellido Barcelona Supercomputing Center (BSC), Spain

General Chair

Jesus Labarta Universitat Politècnica de Catalunya, Spain

Publication Chair

Pedro Valero-Lara Barcelona Supercomputing Center (BSC), Spain

Publicity Chairs

Xavier Teruel Barcelona Supercomputing Center (BSC), Spain Matthijs van Waveren OpenMP Architecture Review Board (ARB)

Local Chair

Xavier Martorell Universitat Politècnica de Catalunya, Spain

Program Committee

Martin Kong Brookhaven National Laboratory, USA Christian Terboven RWTH Aachen University, Germany

Terry Wilmarth Intel, USA

Nasser Giacaman University of Auckland, New Zealand

Alejandro Duran Intel, Spain

Mark Bull EPCC, University of Edinburgh, UK

Chunhua Liao Lawrence Livermore National Laboratory, USA

Stephen Olivier Sandia National Laboratories, USA

James Beyer Nvidia, USA

Thomas R. W. Scogland Lawrence Livermore National Laboratory, USA

Hal Finkel Argonne National Laboratory, USA
Oliver Sinnen University of Auckland, New Zealand

Rosa M. Badia Barcelona Supercomputing Center (BSC), Spain

Mitsuhisa Sato RIKEN AICS, Japan

Eduard Ayguade Universitat Politècnica de Catalunya, Spain

Larry Meadows Intel, USA
Deepak Eachempati Cray Inc., USA

VIII Organization

Joachim Protze RWTH Aachen University, Germany Priya Unnikrishnan IBM Toronto Laboratory, Canada

Eric Stotzer Texas Instruments, USA

IWOMP Steering Committee

Steering Committee Chair

Matthias S. Müller RWTH Aachen University, Germany

Steering Committee

Dieter an Mey RWTH Aachen University, Germany

Eduard Ayguadé BSC and Universitat Politècnica de Catalunya, Spain

Mark Bull EPCC, University of Edinburgh, UK Barbara Chapman Stony Brook University, USA

Bronis R. de Supinski Lawrence Livermore National Laboratory, USA

Rudolf Eigenmann Purdue University, USA
William Gropp University of Illinois, USA

Michael Klemm Intel, Germany

Kalyan Kumaran Argonne National Laboratory, USA

Federico Massaioli CASPUR, Italy Lawrence Meadows Intel, USA

Stephen L. Olivier Sandia National Laboratories, USA

Ruud van der Pas Oracle, USA

Alistair Rendell Australian National University, Australia

Mitsuhisa Sato University of Tsukuba, Japan

Sanjiv Shah Intel, USA

Josemar Rodrigues SENAI Unidade CIMATEC, Brazil

de Souza

Christian Terboven RWTH Aachen University, Germany

Matthijs van Waveren KAUST, Saudi Arabia

Contents

Best	Paper

The Impact of Taskyield on the Design of Tasks Communicating	
Through MPI	3
Loops and OpenMP	
OpenMP Loop Scheduling Revisited: Making a Case for More Schedules Florina M. Ciorba, Christian Iwainsky, and Patrick Buder	21
A Proposal for Loop-Transformation Pragmas	37
Extending OpenMP to Facilitate Loop Optimization	53
OpenMP in Heterogeneous Systems	
Manage OpenMP GPU Data Environment Under Unified Address Space Lingda Li, Hal Finkel, Martin Kong, and Barbara Chapman	69
OpenMP 4.5 Validation and Verification Suite for Device Offload Jose Monsalve Diaz, Swaroop Pophale, Oscar Hernandez, David E. Bernholdt, and Sunita Chandrasekaran	82
Trade-Off of Offloading to FPGA in OpenMP Task-Based Programming Yutaka Watanabe, Jinpil Lee, Taisuke Boku, and Mitsuhisa Sato	96
OpenMP Improvements and Innovations	
Compiler Optimizations for OpenMP	113
Supporting Function Variants in OpenMP	128
Towards an OpenMP Specification for Critical Real-Time Systems	143

OpenMP User Experiences: Applications and Tools	
Performance Tuning to Close Ninja Gap for Accelerator Physics Emulation System (APES) on Intel [®] Xeon Phi TM Processors	163
Visualization of OpenMP* Task Dependencies Using Intel® Advisor – Flow Graph Analyzer	
A Semantics-Driven Approach to Improving DataRaceBench's OpenMP Standard Coverage	
Tasking Evaluations	
On the Impact of OpenMP Task Granularity	205
Mapping OpenMP to a Distributed Tasking Runtime Jeremy Kemp and Barbara Chapman	
Assessing Task-to-Data Affinity in the LLVM OpenMP Runtime Jannis Klinkenberg, Philipp Samfass, Christian Terboven, Alejandro Duran, Michael Klemm, Xavier Teruel, Sergi Mateo, Stephen L. Olivier, and Matthias S. Müller	236
Author Index	253